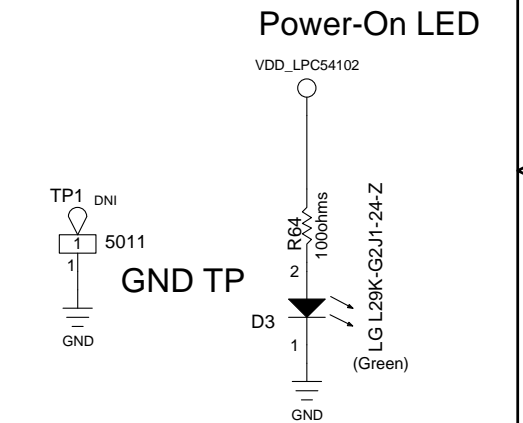
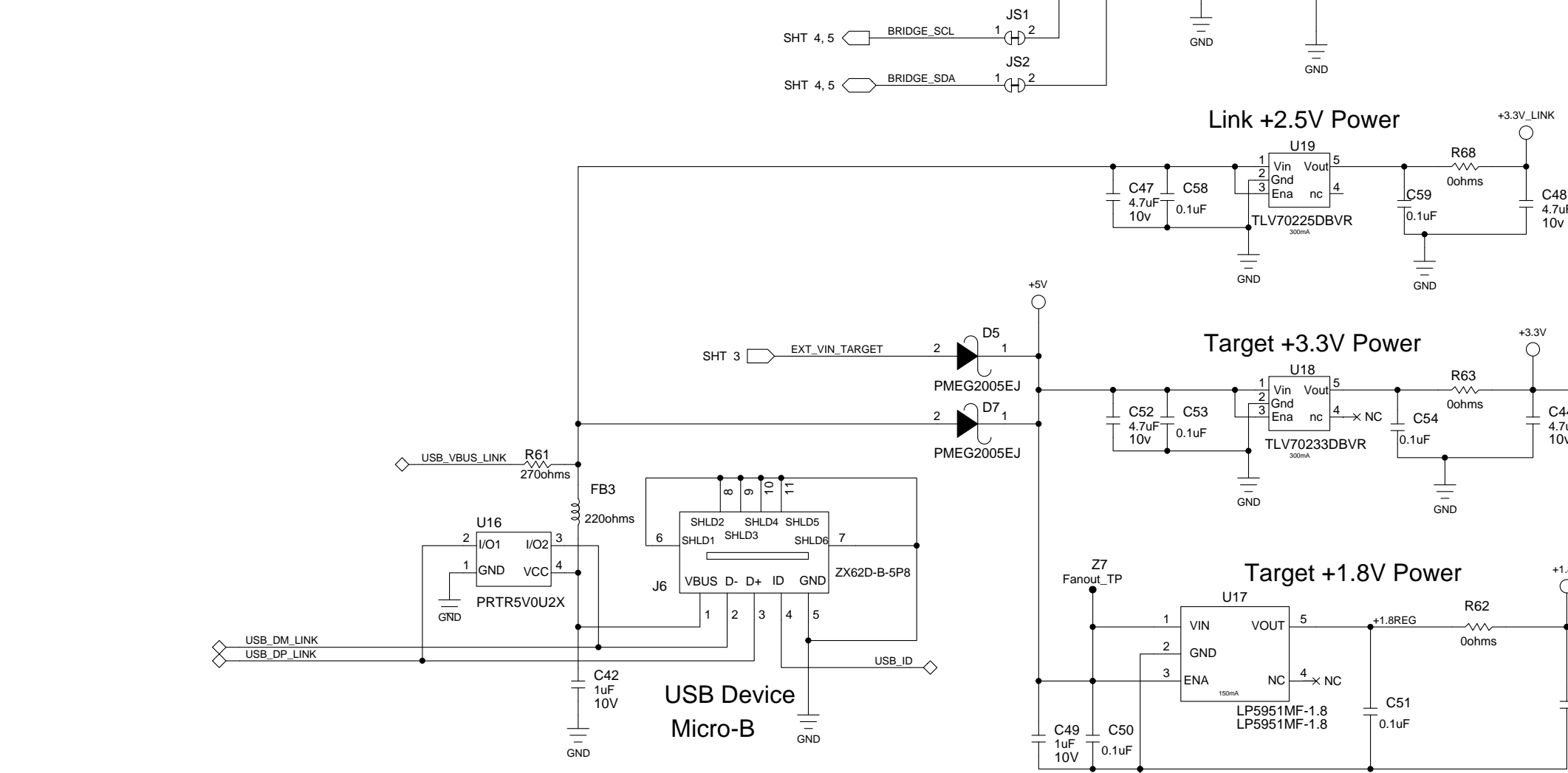
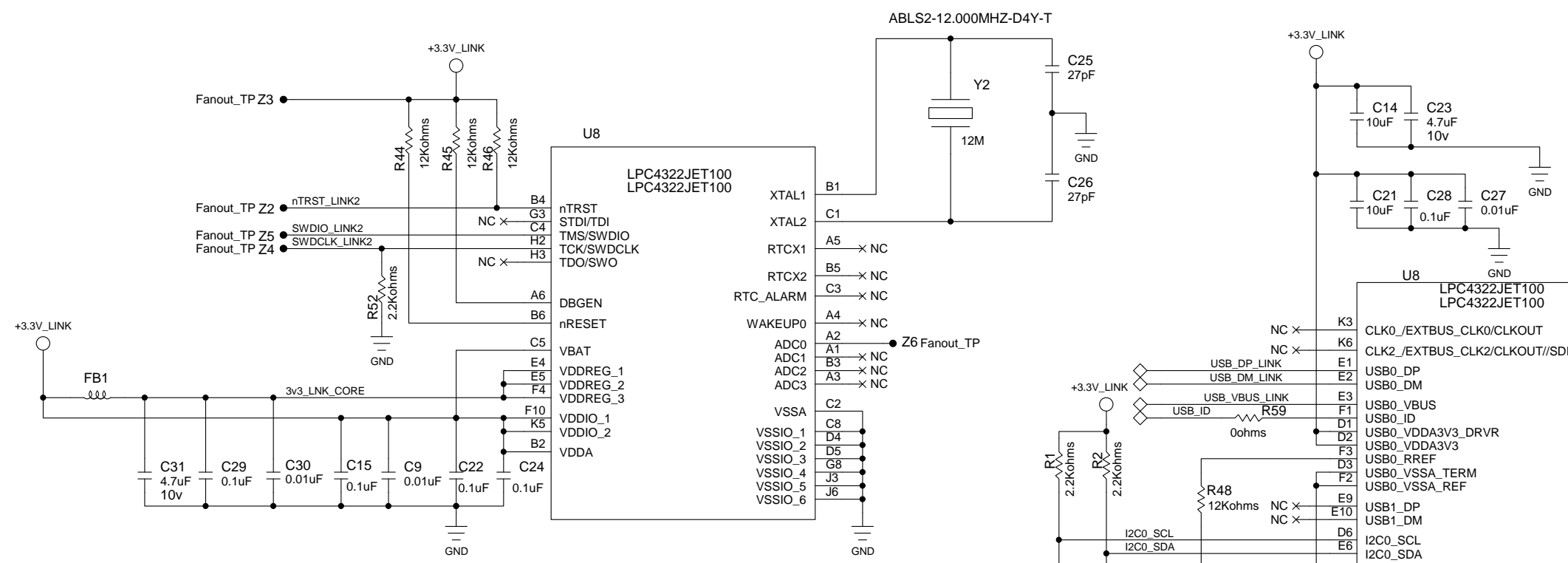


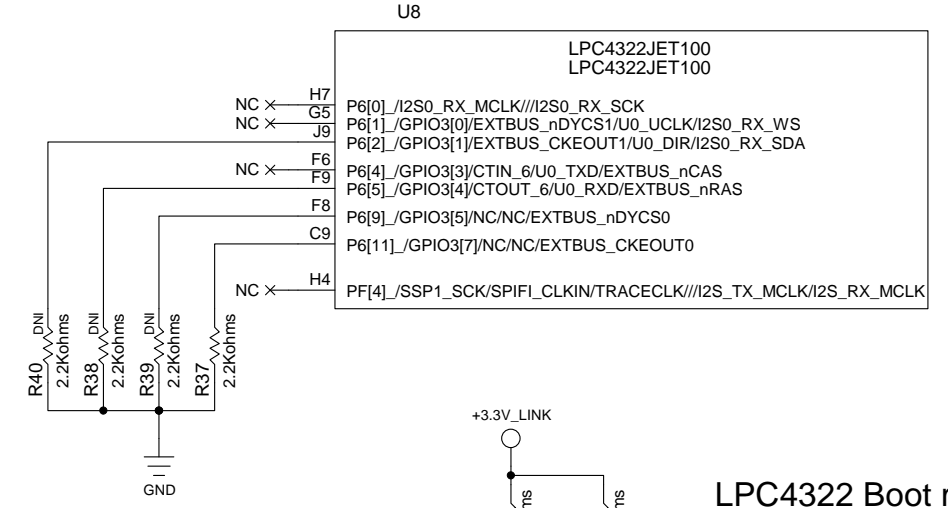
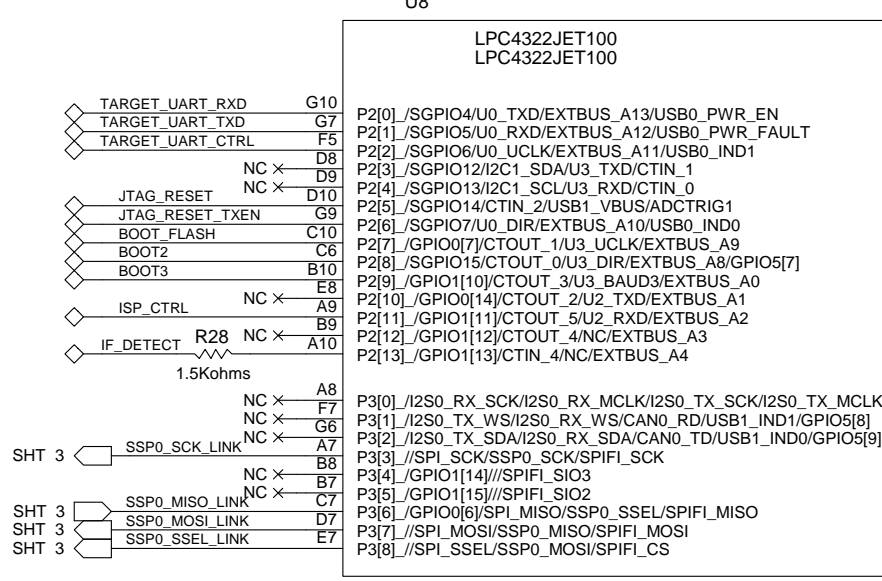
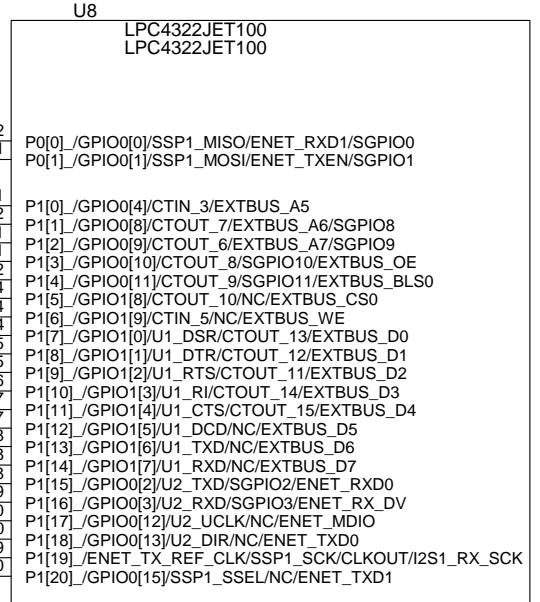
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1	- Prototype LQFP64 board Release	9/04/2014	
A	- Production LQFP64 board release - 74LVC1G53 (U7) VDD changed to +3.3V; sht 3 - Add C60 47pF; sht 3 - Target P0_29 & P0_30 access now at J1; sht 3 - Update LPC54102 current measurement chart; sht 3 - Changed J3 to receptacle 2x06RA; sht 5	9/13/2014	



LPC54102 VDD voltage select JP7  
 VDD = 3.3V; 2 - 3 (default)  
 VDD = 1.8V; 1 - 2

CONTRACT NO.		LINK2 LPC43xx	
APPROVALS	DATE	NXP Semiconductors	
DRAWN	d.consiglio	411 E. Plumeria Dr	
CHECKED		San Jose, CA 95134	
ISSUED	9/13/14	www.standardics.nxp.com/microcontrollers/	
SCALE		SIZE	D
		FSCH NO.	
		DWG. NO.	LPCX54102 LQFP64
		REV	A
SHEET		1 OF 06	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



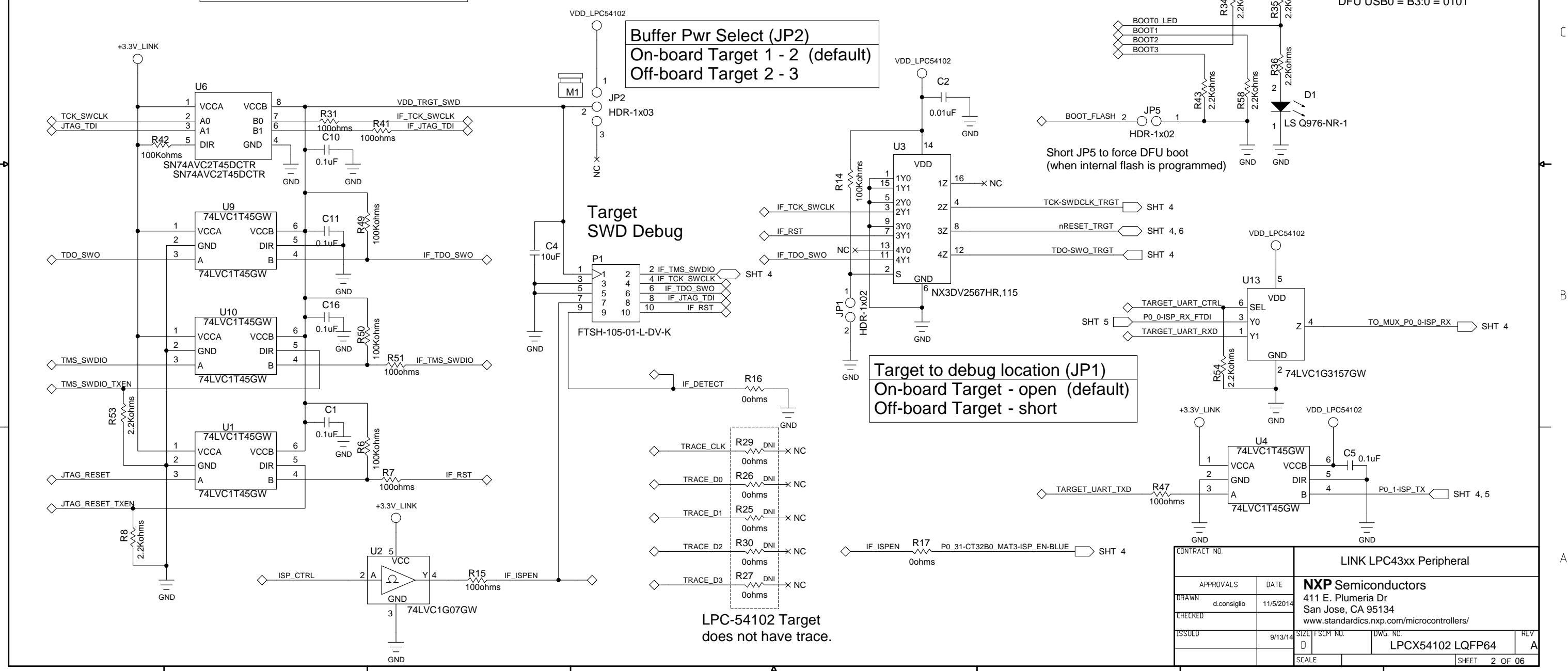
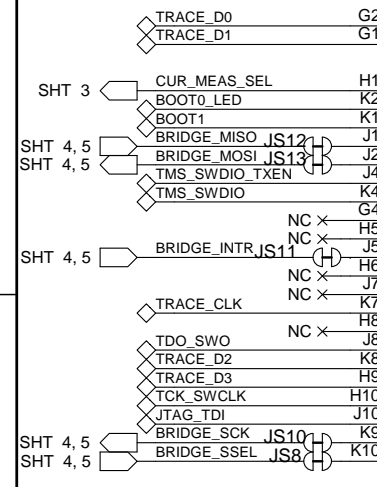
**LPC4322 Boot mode**  
DFU USB0 = B3:0 = 0101

**Buffer Pwr Select (JP2)**  
On-board Target 1 - 2 (default)  
Off-board Target 2 - 3

Short JP5 to force DFU boot  
(when internal flash is programmed)

**Target to debug location (JP1)**  
On-board Target - open (default)  
Off-board Target - short

LPC-54102 Target  
does not have trace.

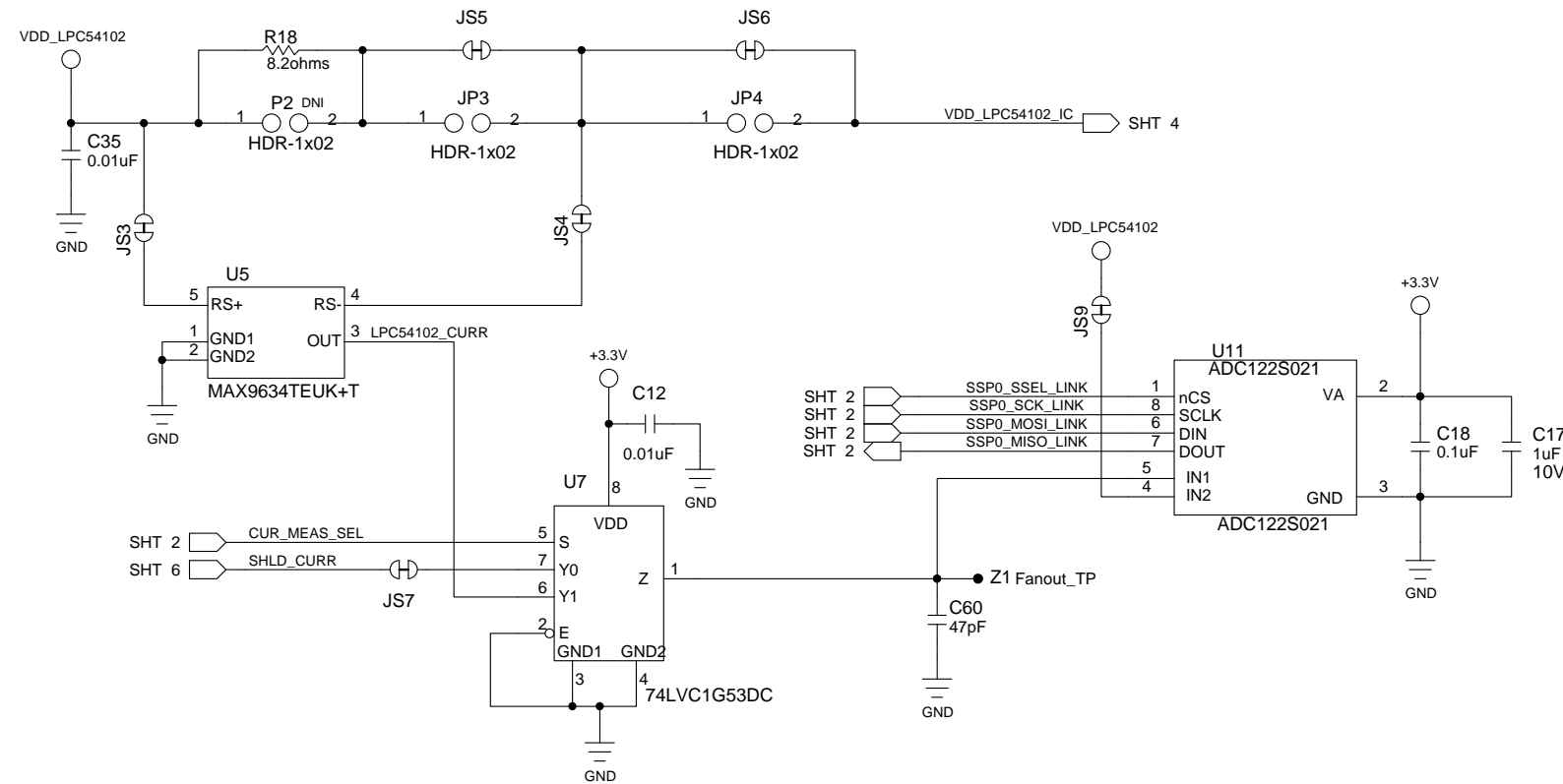
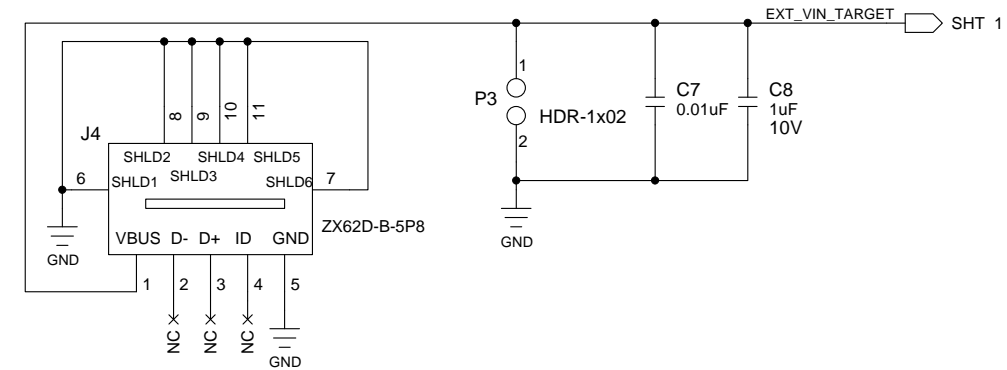


CONTRACT NO.		LINK LPC43xx Peripheral	
APPROVALS	DATE	NXP Semiconductors	
DRAWN d.consiglio	11/5/2014	411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/	
CHECKED		SIZE	DFWG. NO.
ISSUED	9/13/14	D	LPCX54102 LQFP64
		SCALE	SHEET 2 OF 06

LPC54102 Current measurement

LPC54102			ADC111S021 12bit ADC
Vsense R18 voltage 1-lsb	LPC54102 Current 1-lsb	maximum current	ADC input 1- lsb
32uV	3.9uA	16mA	800uV

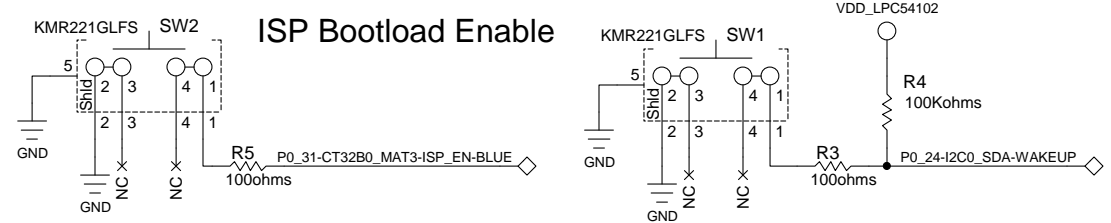
Target External Vin 5V



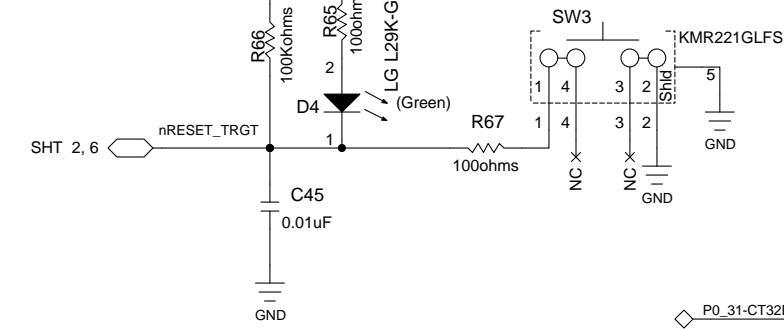
CONTRACT NO.		Target LPC54102 power & current measurement			
APPROVALS	DATE	<b>NXP Semiconductors</b> 411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/			
DRAWN	11/5/2014				
CHECKED					
ISSUED	9/13/14	SIZE	FSCM NO.	DWG. NO.	REV
		D		LPCX54102 LQFP64	A
SCALE				SHEET 3 OF 06	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

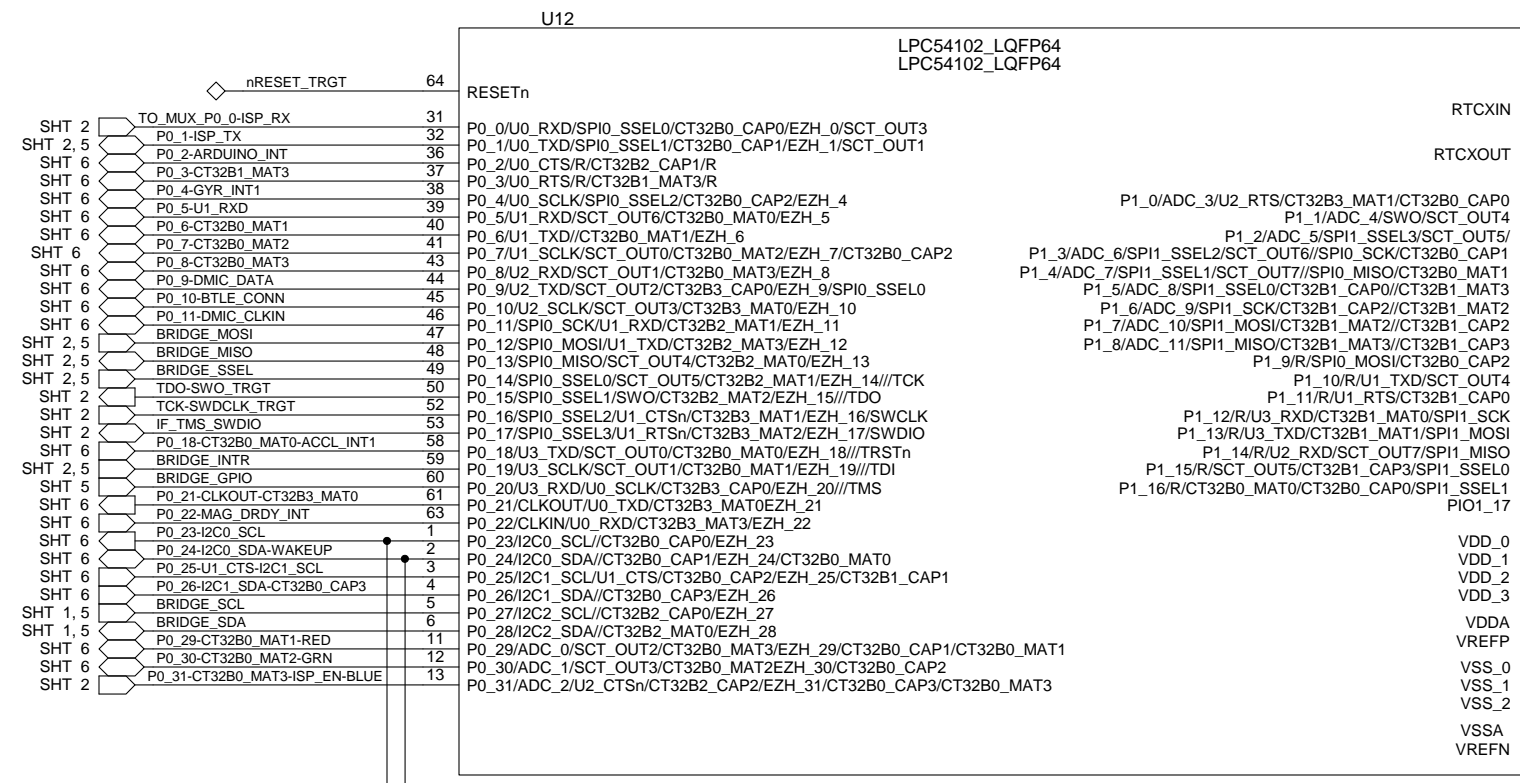
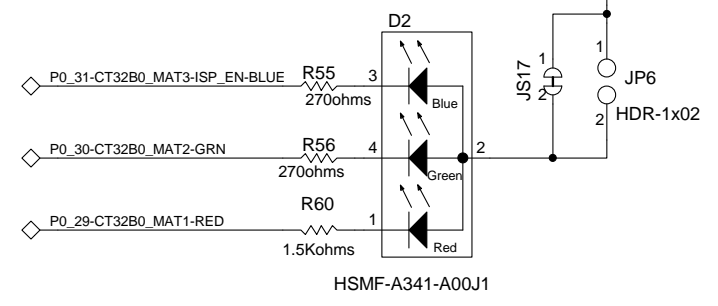
### WAKEUP



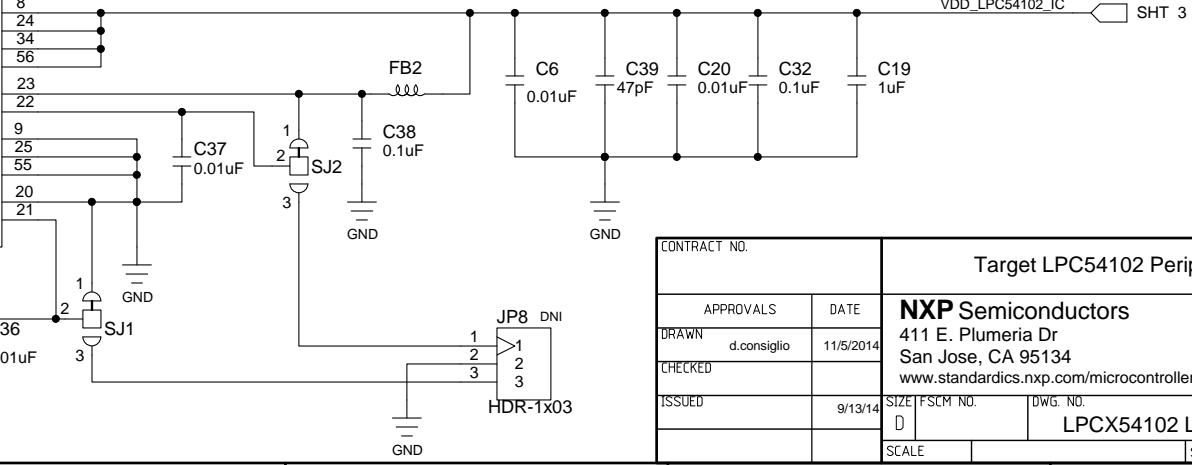
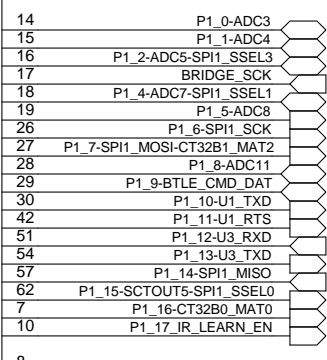
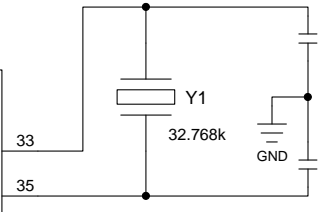
### RESET



### RGB LED



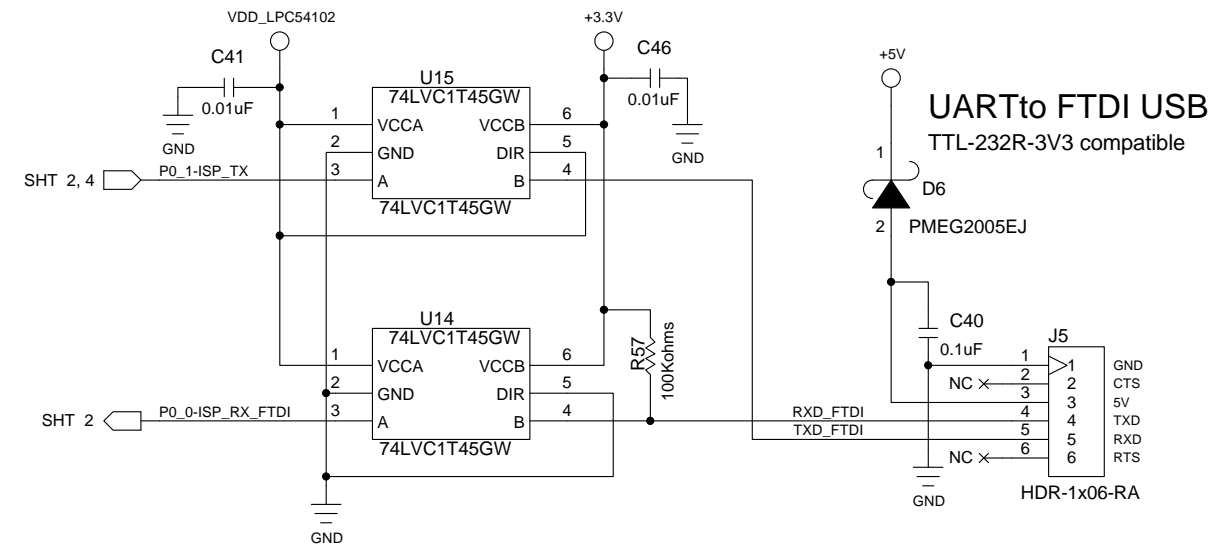
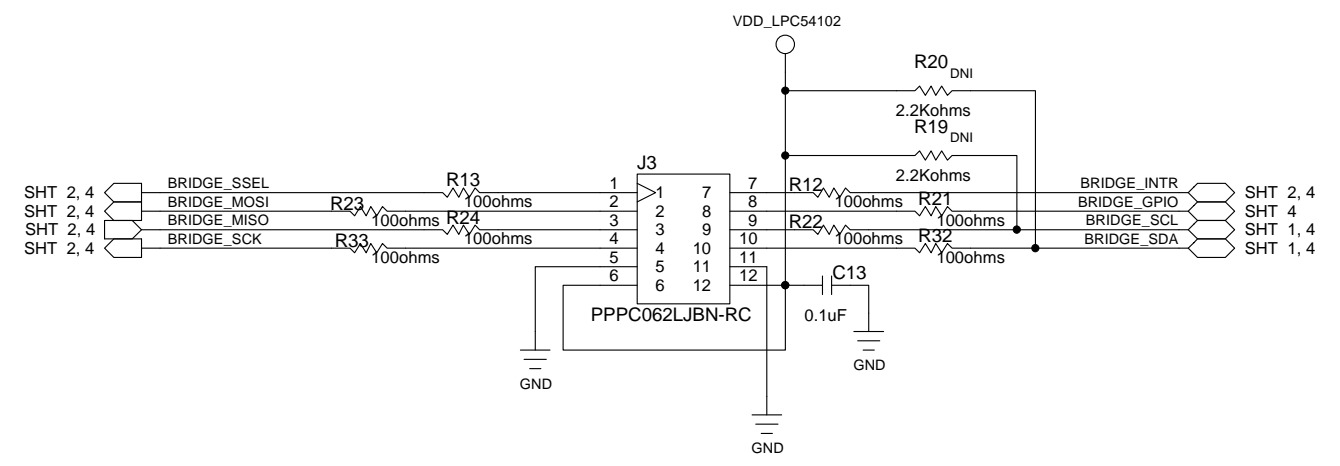
### SSPT7F-12.5PF20-R



CONTRACT NO.		Target LPC54102 Peripheral	
APPROVALS	DATE	<b>NXP Semiconductors</b>	
DRAWN	11/5/2014	411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/	
CHECKED		SIZE/FSCM NO.	DWG. NO.
ISSUED	9/13/14	D	LPCX54102 LQFP64
SCALE		SHEET 4 OF 06	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

### SPI / I2C brdge header (Pmod compatible)



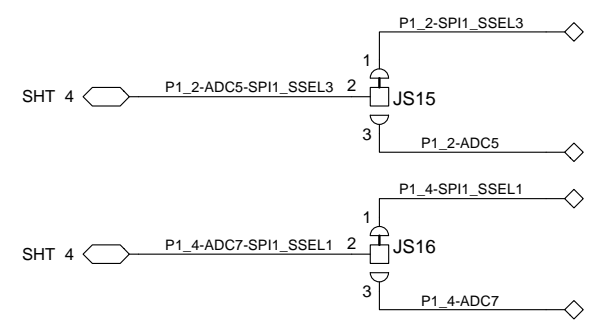
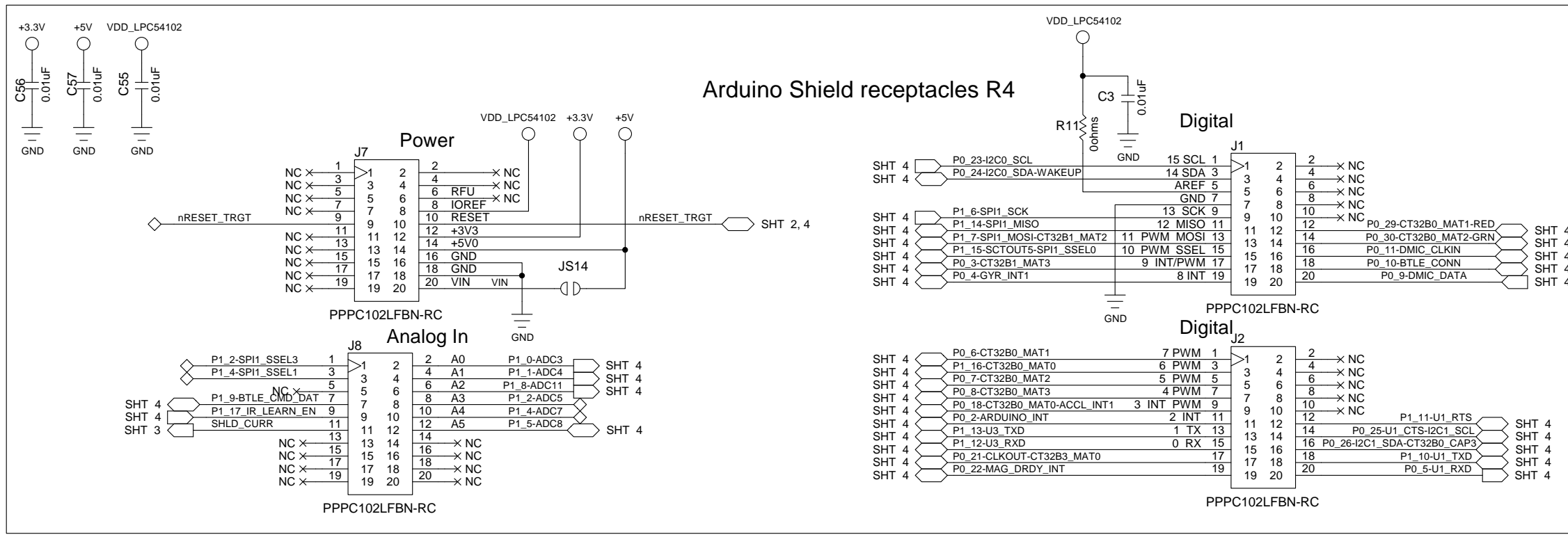
### UART to FTDI USB

TTL-232R-3V3 compatible

CONTRACT NO.		I2C_SPI bridge hdr / FTDI			
APPROVALS	DATE	<b>NXP Semiconductors</b> 411 E. Plumeria Dr San Jose, CA 95134 <a href="http://www.standardics.nxp.com/microcontrollers/">www.standardics.nxp.com/microcontrollers/</a>			
DRAWN	d.consiglio				
CHECKED					
ISSUED	9/13/14	SIZE	D	FSCM NO.	LPCX54102 LQFP64
SCALE		DWG. NO.		REV	A
				SHEET 5 OF 06	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

### Arduino Shield receptacles R4



CONTRACT NO.		Arduino Interface			
APPROVALS	DATE	<b>NXP Semiconductors</b>			
DRAWN	d.consiglio	411 E. Plumeria Dr San Jose, CA 95134			
CHECKED		www.standardics.nxp.com/microcontrollers/			
ISSUED	9/13/14	SIZE	FSCM NO.	DWG. NO.	REV
		D		LPCX54102 LQFP64	A
		SCALE		SHEET	6 OF 06