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Revisions & Change Log

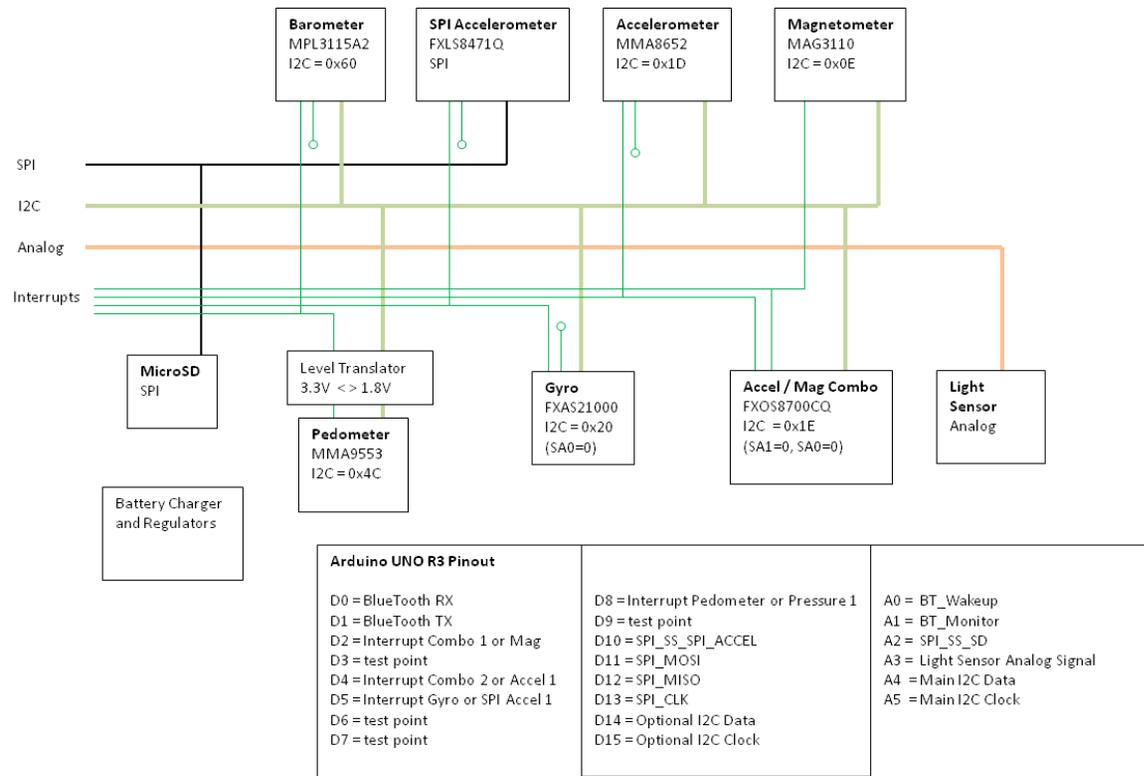
Rev	Description	Date	Approved
X1	27909 base Initial Draft	03/07/13	J. SCOTT
A	27909 Prototype Release	03/26/13	J. SCOTT
AX1	27982 (BT Version) Under Development	07/16/13	J. SCOTT
AX1	27982 (BT Version) PPL & A070 RELEASE	07/22/13	J. SCOTT
AX2	27982 (BT Version) Block Diagram Update	08/07/13	J. SCOTT
B	27982 (BT Version) Release candidate	08/23/13	J. SCOTT
BX1	27982 (UNDER DEVELOPMENT) I2C swap fix	09/25/13	J. SCOTT
C	27909 Non BT ver. (BOM update per coreid) Release to prod.	10/02/13	J. SCOTT

FRDM-FXS-MULTI

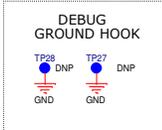
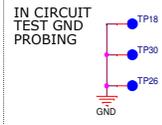
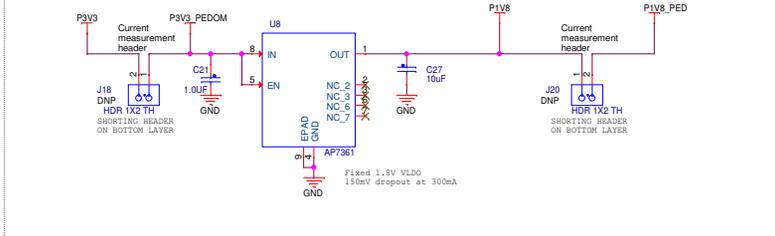
		Automotive, Industrial & Multi-Market Solutions Group 6501 William Cannon Drive West Austin, TX 78735-6598	
ICAP Classification: FCP: FLUQ: PUB: X			
Designer: RAFAEL DEL REY		Drawing Title: FRDM-FXS-MULTI	
Drawn by: RAFAEL DEL REY		Page Title: TITLE PAGE	
Approved: JAMES SCOTT	Size C	Document Number SCH-27909 PDF: SPF-27909	Rev C
Date: Wednesday, October 02, 2013 Sheet 1 of 4			

- Unless Otherwise Specified:
 All resistors are in ohms, 5%, 1/8 Watt
 All capacitors are in uF, 20%, 50V
 All voltages are DC
 All polarized capacitors are aluminum electrolytic
- Interrupted lines coded with the same letter or letter combinations are electrically connected.
- Device type number is for reference only. The number varies with the manufacturer.
- Special signal usage:
 _B Denotes - Active-Low Signal
 <> or [] Denotes - Vectored Signals
- Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

FRDM-FXS-MULTI Block Diagram



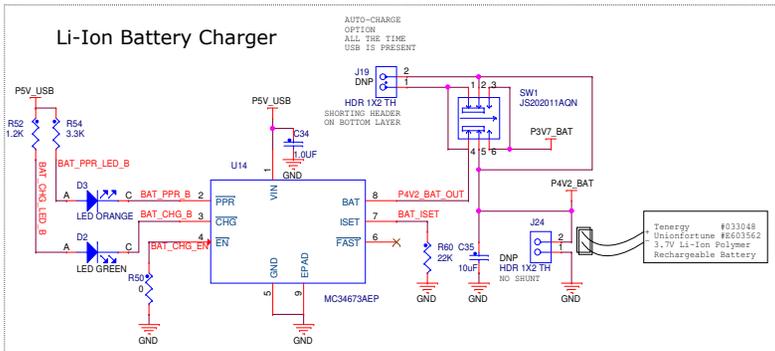
VOLTAGE REGULATION



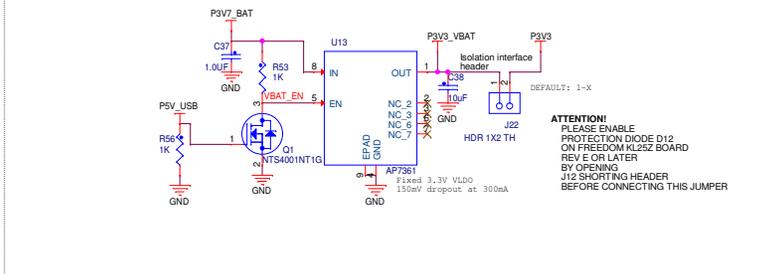
Prototyping Area Heading Signals

pg(3) I2C_SCL0	<<>	I2C_SCL0	D15	TP9
pg(3) I2C_SDA0	<<>	I2C_SDA0	D14	TP10
pg(3,4) SPL_CLK	<<>	SPL_CLK	D13	DNP
pg(3,4) SPI_MISO	<<>	SPI_MISO	D12	TP13
pg(3,4) SPI_MOSI	<<>	SPI_MOSI	D11	TP14
pg(3) SPI_SS_SPI_ACCEL	<<>	SPI_SS_SPI_ACCEL	D10	TP15
pg(3) TP_FRDM_DIG09	<<>	TP_FRDM_DIG09	D9	DNP
pg(3) INT_PED	<<>	INT_PED	D8	TP17
pg(3) TP_FRDM_DIG07	<<>	TP_FRDM_DIG07	D7	DNP
pg(3) TP_FRDM_DIG06	<<>	TP_FRDM_DIG06	D6	DNP
pg(3) INT_GYRO	<<>	INT_GYRO	D5	TP3
pg(3) INT_ACCEL	<<>	INT_ACCEL	D4	TP4
pg(3) TP_FRDM_DIG03	<<>	TP_FRDM_DIG03	D3	DNP
pg(3) INT_MAG	<<>	INT_MAG	D2	TP6
pg(3) BT_TX0	<<>	BT_TX0	D1	TP7
pg(3) BT_RX0	<<>	BT_RX0	D0	TP8
pg(3) I2C_SCL1	<<>	I2C_SCL1	A5	DNP
pg(3) I2C_SDA1	<<>	I2C_SDA1	A4	TP19
pg(3) SNS_LIGHT	<<>	SNS_LIGHT	A3	TP29
pg(3,4) SPI_SS_SD	<<>	SPI_SS_SD	A2	DNP
pg(3) BT_MONITOR	<<>	BT_MONITOR	A1	TP36
pg(3) BT_WAKEUP	<<>	BT_WAKEUP	A0	TP37
				TP11
				DNP

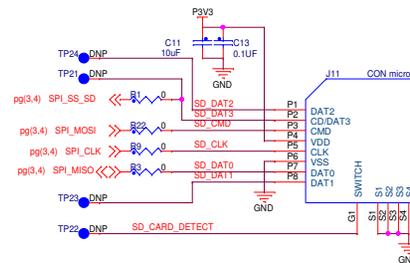
Li-Ion Battery Charger



Battery Regulation



microSD Card Connector, SPI Mode



ICAP Classification:	FCP:	FIUC: X	PUBI:
Drawing Title:			
FRDM-FXS-MULTI			
Page Title:			
POWER SUPPLY			
Size	Document Number	SCH-27909 PDF: SPF-27909	Rev C
C			
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