

Table of Contents

1	Title
2	Notes
3	Sensors

Revisions

Rev	Description	Date
A	Original Release	10-24-15

BRKTSTBC-A8471

		RF, Analog & Sensor Group	
		6501 William Cannon Drive West Austin, TX 78752-8369	
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ICAP Classification:		FCP:	FIUC: X PUB:
Designer: Aswin Sivakumar	Drawing Title: BRKTSTBC-A8471		
Drawn by: Aswin Sivakumar	Page Title: TITLE PAGE		
Approved: Team	Size C	Document Number SCH-28975 PDF: SPF-28975	Rev A
Date: Saturday, October 24, 2015		Sheet 1 of 3	

1. Unless Otherwise Specified:

All resistors are in ohms, most are 1%, 1/10 Watt. Otherwise are 5%, 1/8 Watt.

All capacitors are in uF, some are 10% or 20%

All voltages are DC

All polarized capacitors are tantalum

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Unpopulate R7 and populate R6 to use die heater.

4. Device's default communication -SPI Interface. Populate R8,R9,R11(or R3)- SA1,R2(or R4)- SA0 for I2C interface.

5. Populate C4 if FXOS8700 is used instead of FXLS8471

6. Device type number is for reference only. The number varies with the manufacturer.

7. Special signal usage:

_B Denotes - Active-Low Signal

<> or [] Denotes - Vectored Signals

8. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



ICAP Classification: FCP: FUC: X PUB:

Drawing Title: **BRKTSTBC-A8471**

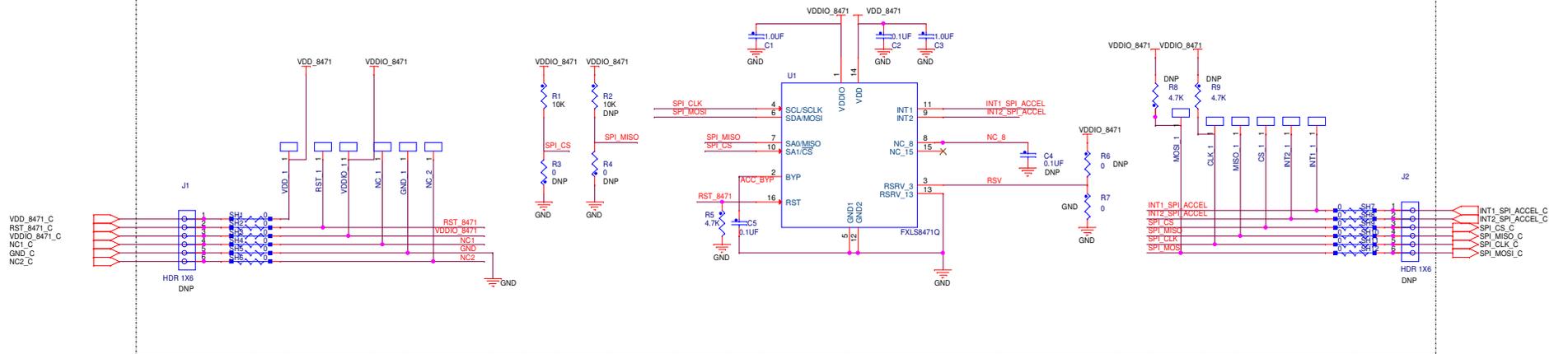
Page Title: **NOTES**

Size Document Number Rev
C SCH-28975 PDF: SW-28975 A

Date: Saturday, October 24, 2015 Sheet 2 of 3

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FXLS8471Q SPI ACCELEROMETER



VDD_8471_C
RST_8471_C
VDDIO_8471_C
NC1_C
GND_C
NC2_C



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Drawing Title: BRKTSTBC-A8471	
Page Title: sensors	
Size C	Document Number SCH-28975 PDF: SPY-28975
Date: Saturday, October 24, 2015	Sheet 3 of 3

Rev A