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1Revisions			
Rev	Description	Date	Approved
A	Initial Release	8/11/2015	AQ
A1	Update antenna match	1/20/2016	AQ

FRDM-KW019032

		Wireless Connectivity Operation 6501 William Cannon Drive West Austin, TX 78735-8598	
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Designer: A. Quiroz		ICAP Classification: FCP: FIUQ: X PUBI:	
Drawing Title: AVID		FRDM-KW019032	
Page Title: AVID		TITLE PAGE	
Approved: A. Quiroz		Size C	Document Number SCH-28745
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TODO: UPDATE REFDES and NOTES

1. Unless Otherwise Specified:

All resistors are in ohms, 5%, 1/8 Watt

All capacitors are in μF , 20%, 50V

All voltages are DC

All polarized capacitors are aluminum electrolytic

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Device type number is for reference only. The number varies with the manufacturer.

4. Special signal usage:

_B Denotes - Active-Low Signal

<> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

6. Using this board as an Arduino Host may require populating one or more of the following bottom side resistors:

one or more of the following bottom side re

- To source 3.3V to J3.10: populate R12

- To source 5V to J3.12: populate R140
- To source J3.6 and J3.5: populate R139 (5V) or R138 (3.3V)

- To source J3.6 and IO_REF: populate P128
- To source 3.3V to J2.16: populate P128



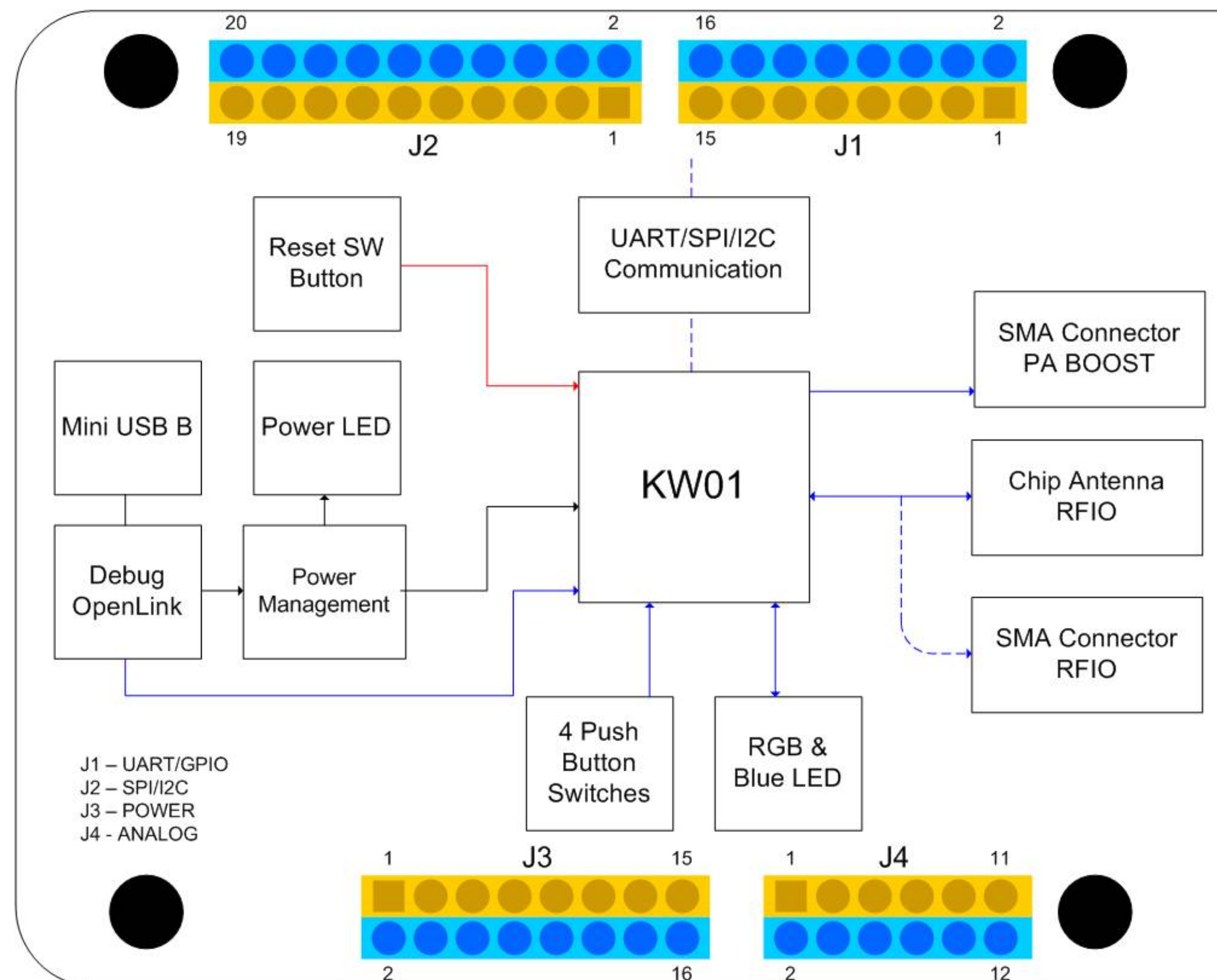
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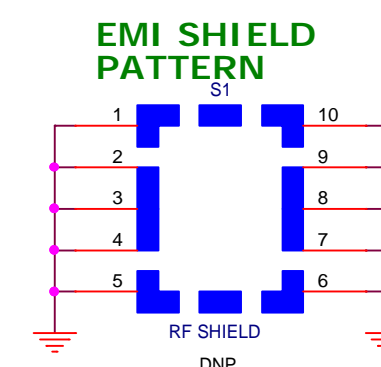
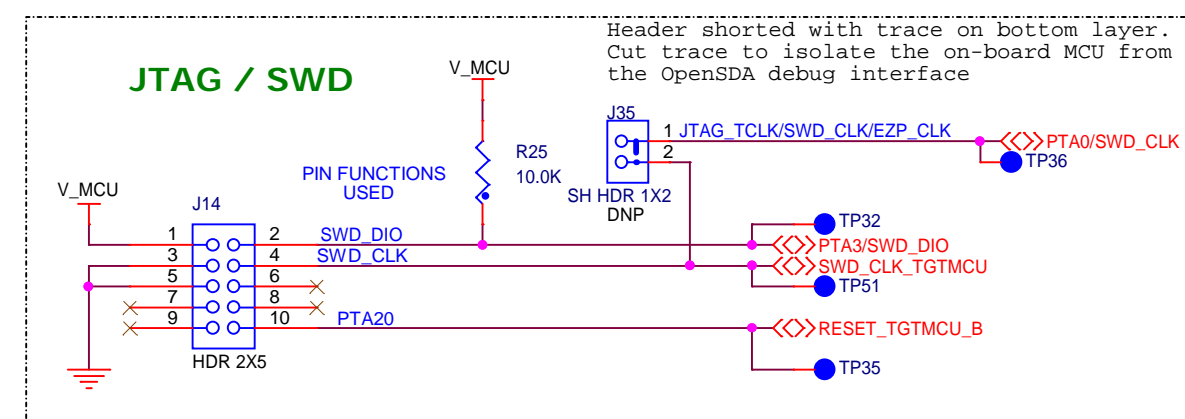
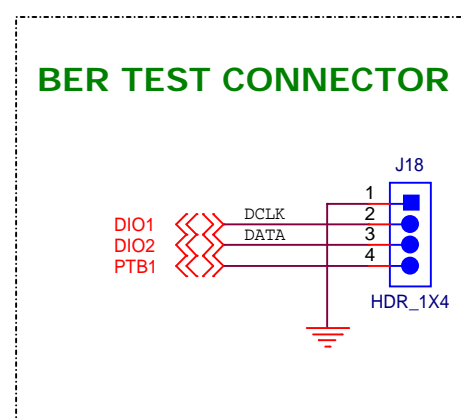
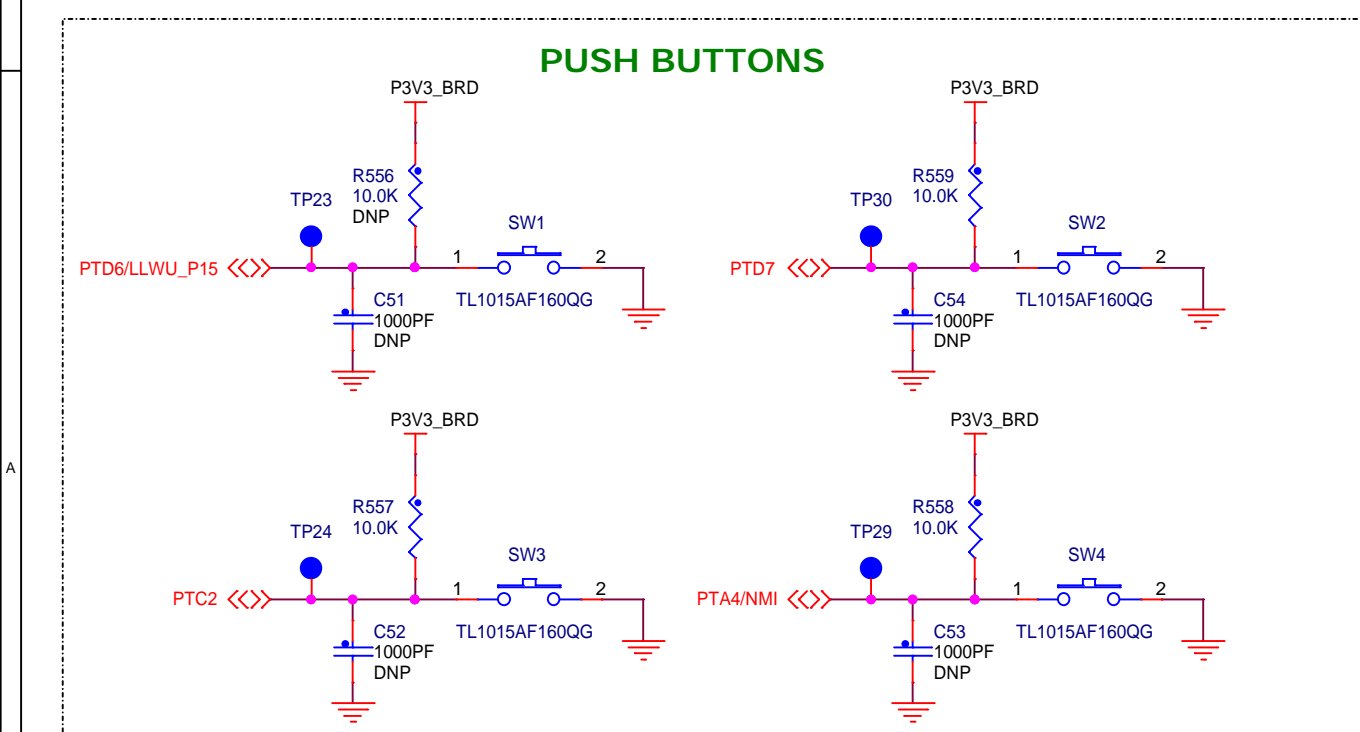
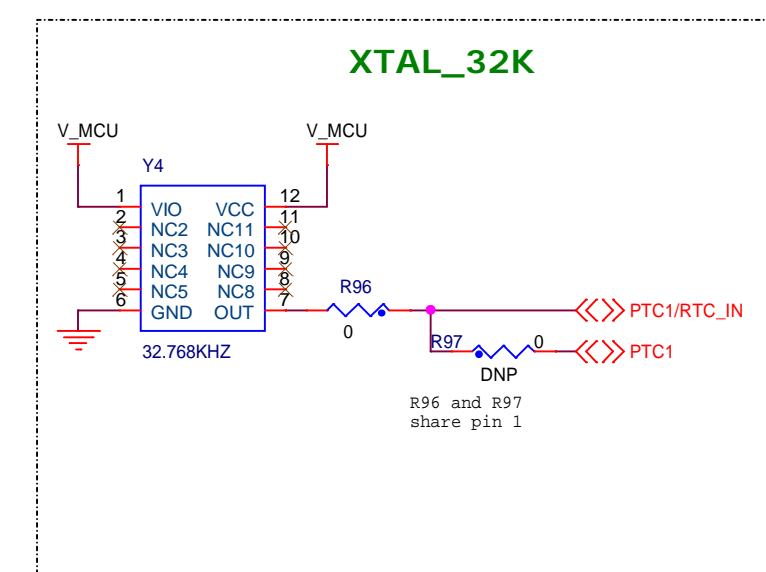
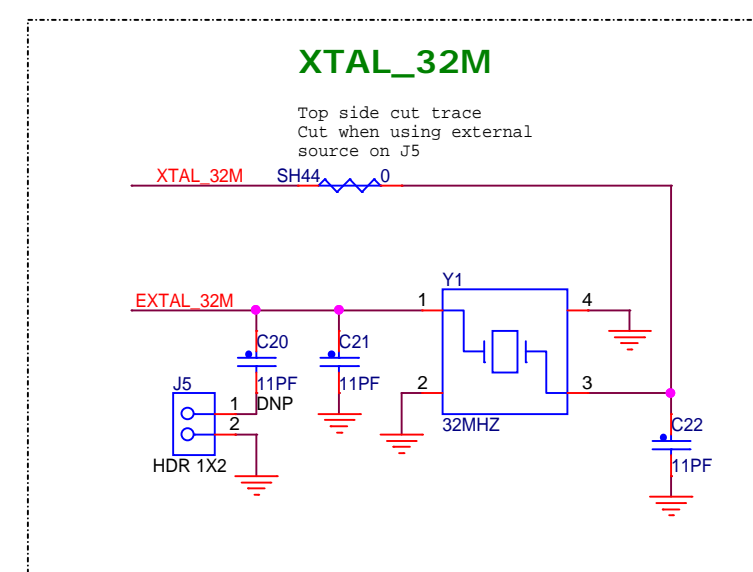
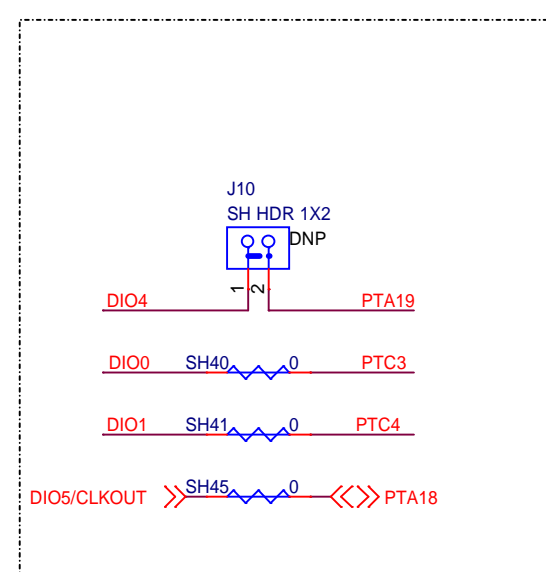
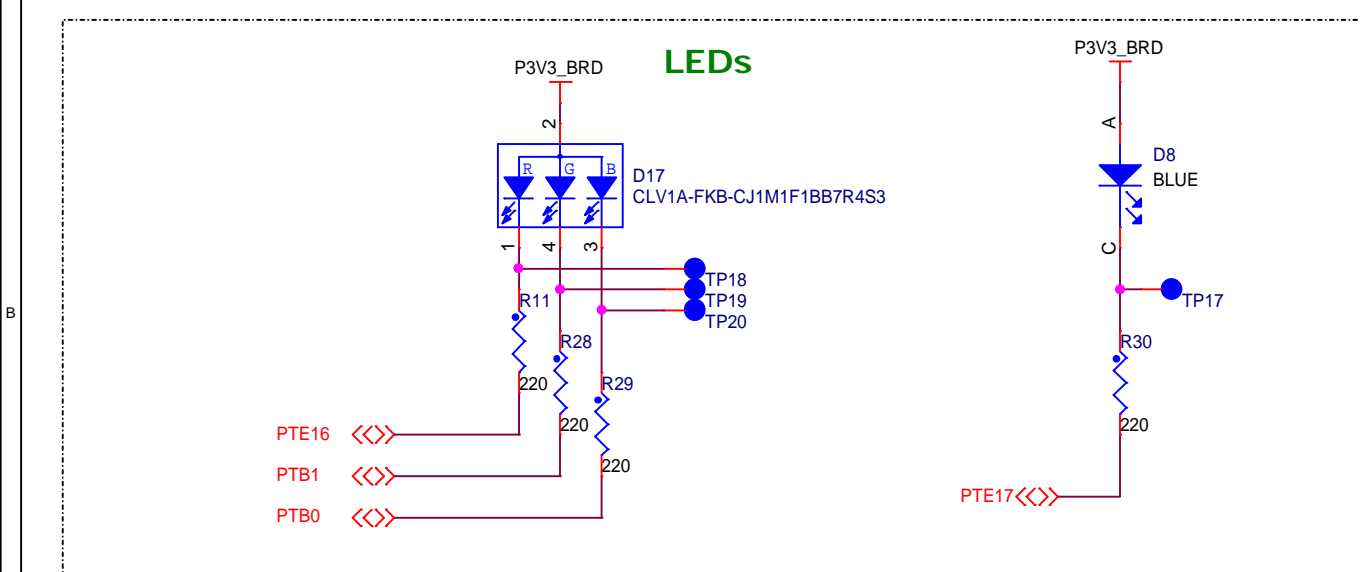
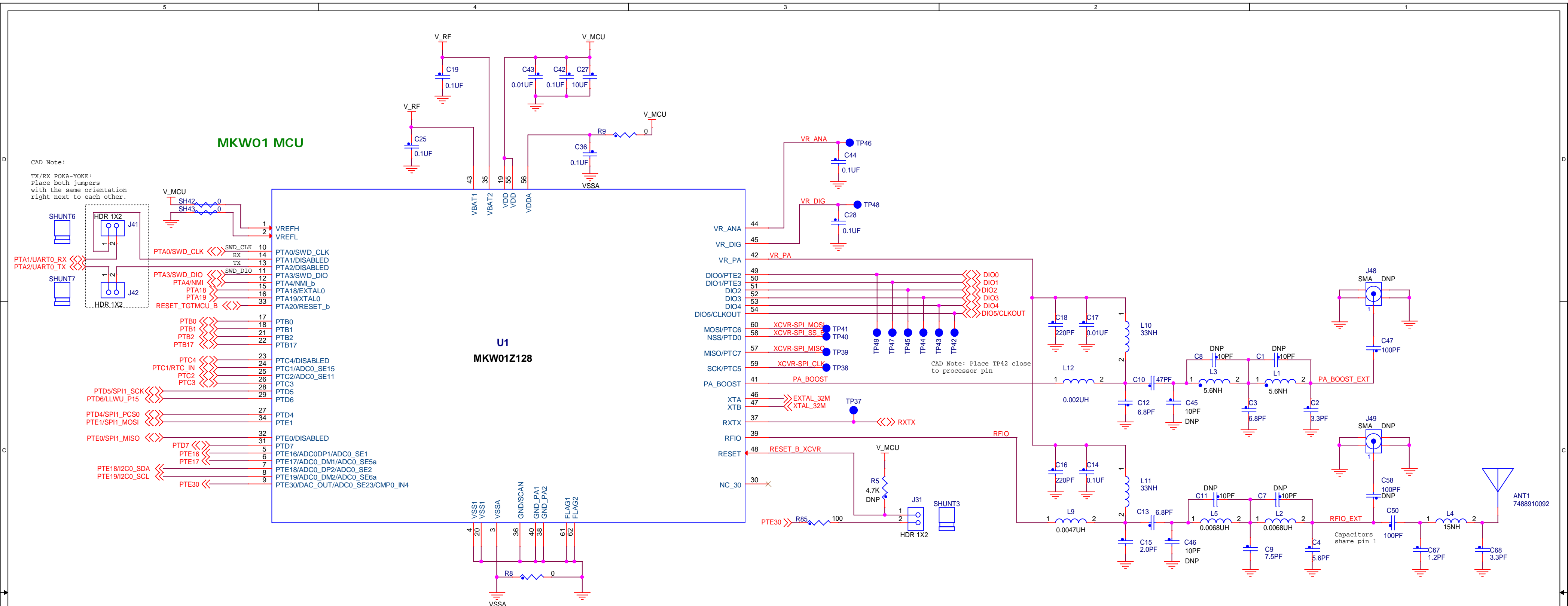
ICAP Classification:	FCP:	FI00: X
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Page Title: **NOTES**

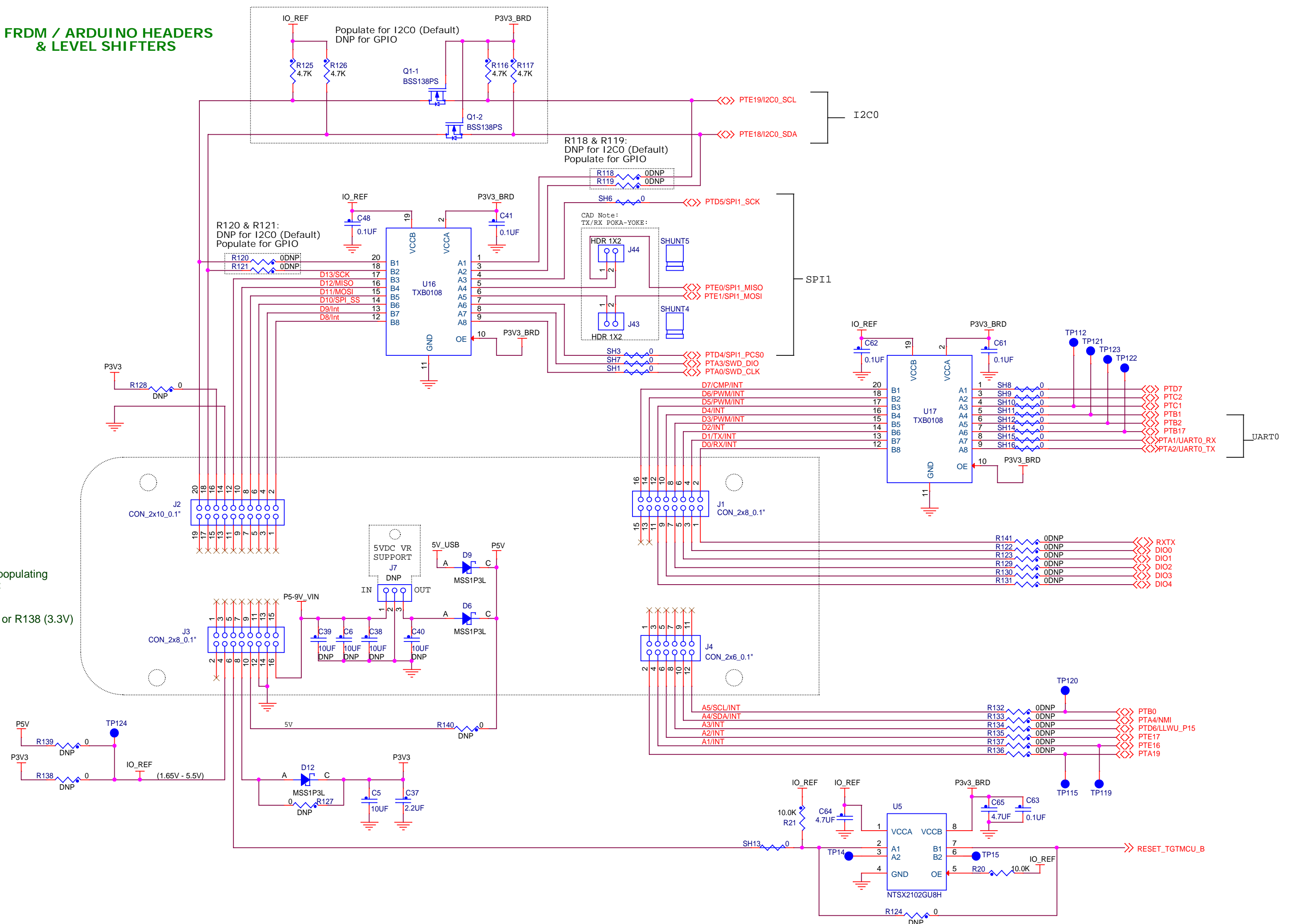
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FRDM / ARDUINO HEADERS & LEVEL SHIFTERS



POWER MANAGEMENT

