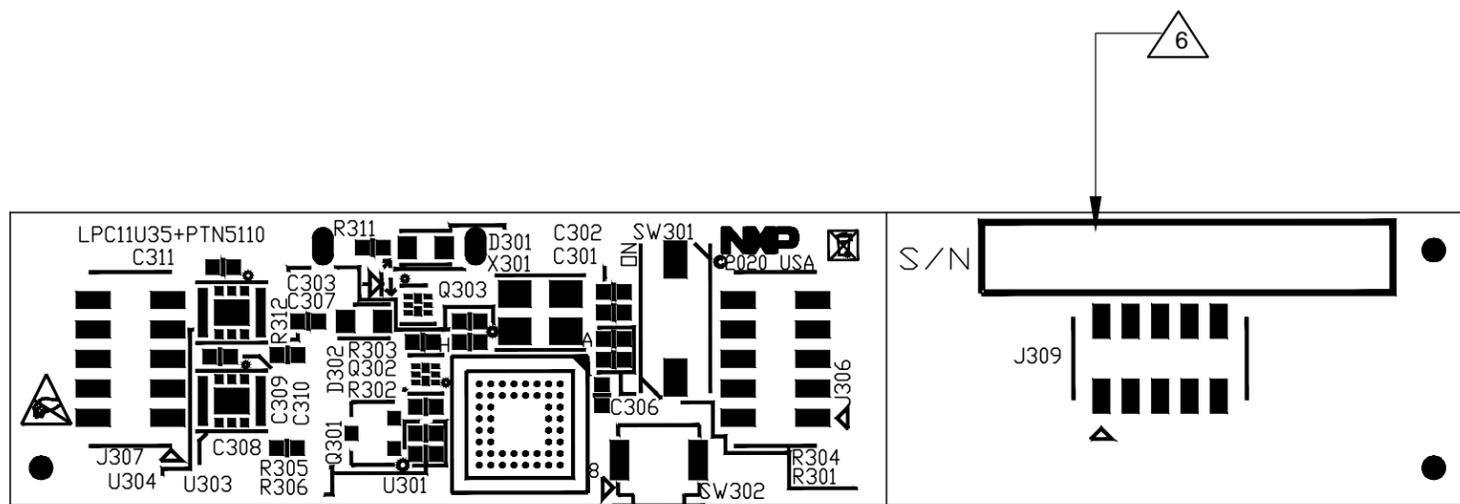


REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	X1	PROTO RELEASE	11/24/20	B.Z

NOTES : TRIANGLE SYMBOLS IDENTIFY NOTES ON THIS DRAWING.



NOTES:

1. ALL COMPONENTS MUST BE ABLE TO WITHSTAND THE Pb-FREE ASSEMBLY PROFILES DESCRIBED IN IPC/JEDEC J-STD-020C (LATEST VERSION)
2. WORKMANSHIP SHALL CONFORM TO IPC-A-610 (LATEST REVISION) - CLASS 2 STANDARD.
3. PART SUBSTITUTION CRITERIA :-
ALL COMPONENT SUBSTITUTIONS MUST BE APPROVED AND DOCUMENTED BY NXP.
4. MAXIMUM LEAD PROTRUSION ON PRIMARY AND SECONDARY SIDE OF PRINTED WIRING BOARD IS .100".
5. IN CASES OF CONFLICT, ALL PLACEMENT LOCATIONS ARE TO BE GOVERNED BY THE X-Y CENTROID FILE, UNLESS EXPLICITLY STATED IN THIS ASSEMBLY DRAWING. SILKSCREEN REFERENCE DESIGNATORS ARE INCLUDED FOR CONVENIENCE ONLY.

6. PLACE PRINTED SERIAL NUMBER LABEL (805-76760) WHERE SHOWN.

PRIMARY SIDE

RoHS : ALL PARTS, MATERIALS AND FINISHED ASSEMBLY SHALL MEET THE RoHS COMMISSION DELEGATED DIRECTIVE (EU) 2015 / 863 OF MARCH 31 2015 AMENDING ANNEX II TO DIRECTIVE 2011 / 65 / EU. A CERTIFICATE OF COMPLIANCE IS REQUIRED UPON REQUEST.

REACH : ALL PARTS, MATERIALS AND FINISHED ASSEMBLY SHALL NOT CONTAIN ANY OF THE SUBSTANCES OF VERY HIGH CONCERN (SVHC) ABOVE THE THRESHOLD VALUE PER THE CURRENT ECHA LIST OF SVHC'S, AND WITH THE ANNEX XIV OF REACH DIRECTIVE 76 / 769 / EEC.. A CERTIFICATE OF COMPLIANCE IS REQUIRED UPON REQUEST.

SOLDER PROCESS	
Pb FREE	<input checked="" type="checkbox"/> YES <input type="checkbox"/> NO
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES. TOLERANCES ARE: DECIMALS .XX ±.020 .XXX ±.010 ✓ RMS ALL MACHINED SURFACES BREAK ALL SHARP EDGES AND CORNERS, REMOVE BURRS. UNDERLINED DIM. NOT TO SCALE. THIRD ANGLE ORTHOGRAPHIC PROJECTION IS USED.	PART NO. 700-47770 THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO NXP AND SHALL NOT BE USED FOR ENGINEERING DESIGN, PROCUREMENT OR MANUFACTURE IN WHOLE OR IN PART WITHOUT THE CONSENT OF NXP --- PUI (PUBLIC INFORMATION) ..X. FIUD (NXP INTERNAL USE ONLY) --- FCP (FREESCALE CONFIDENTIAL PROPRIETARY)
MATERIAL	APPROVALS
FINISH	DATE
HEAT TREAT	
DRAWN DEEPEP V	11/24/20
CHECKED BOBBY Z	11/24/20
DESIGN ENGINEER HELEN X	11/24/20

NXP Semiconductors 6501 WILLIAM CANNON DRIVE AUSTIN, TEXAS 78735-8598 USA

TITLE
PWB ASSEMBLY
X-PTN38007-BASE

SIZE B	FILE NAME ASY-47770.DXF	DWG. NO. ASY-47770	REV X1
SCALE 1:1	DO NOT SCALE DRAWING	SHEET 1 OF 2	

DWG. NO. ASY-47770 REV X1

