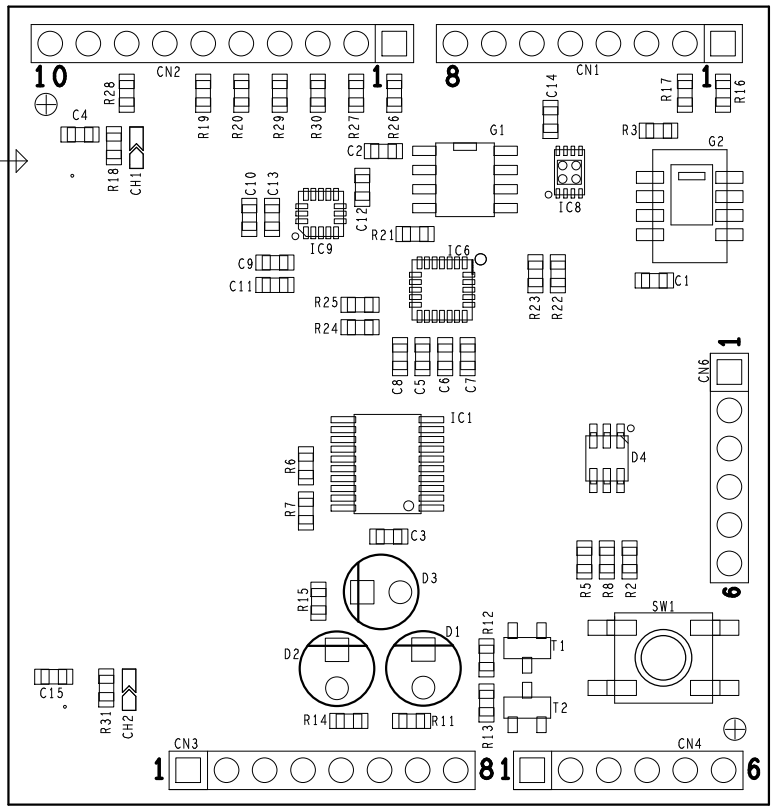

Date	Draw by	Verify	Modifications	IND.

Date : 20/07/16 Draw by : ASCO-SN Checked by:V.FILLATRE Project : JN518X	Material : . File.	Marging : +/-0.1	Nbr of layers :
	Finition : .	Manufact.level : .	Silkscreen TOP :. BOTTOM:.
	Protection:.	Scale : 2/1	

NXP Semiconductors
2,Esplanade Anton Philips
Campus Effiscience Colombelles
BP 2000 - 14906 CAEN CEDEX 9

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ASSEMBLY DRAWING TOP/BOTTOM OM15081-1
JN518X AUDIO SENSORS EXPANSION BOARD

SIZE		REV.	PL.
A4	PCB2460-1-400	00	1/1