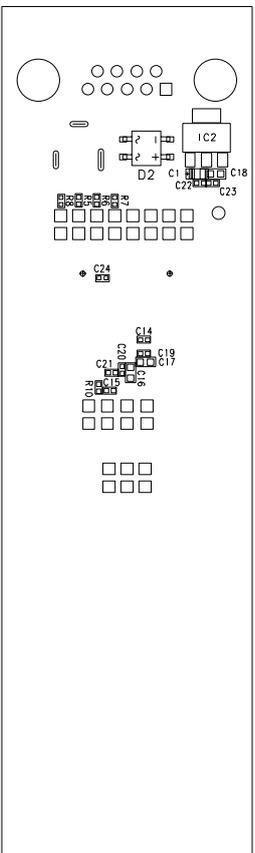


WARNING: DO NOT CONNECT TB2

PCB1643-1 PN531\_HSU TOP COMPONENTS



PCB1643-1 PN531\_HSU BOTTOM COMPONENTS

WARNING: BY DEFAULT, HSU INTERFACE IS USED  
REFER TO THE TABLE BELOW AND TO THE INTERFACE REQUESTED BY THE CUSTOMER

INTERFACE	STRAPPS	
	CONNECTED = 0 Ohm	NOT CONNECTED
HSU	ST5 ST6 ST2 ST9	ST3 ST7 ST8
I2C	ST6 ST2 ST9	ST3 ST5 ST7 ST8
SPI mode 0	ST3 ST5 ST9	ST6 ST7 ST8
SPI mode 1	ST3 ST8 ST9	ST2 ST5 ST6 ST7
SPI mode 2	ST3 ST7 ST9	ST2 ST5 ST6 ST8
SPI mode 3	ST3 ST7 ST8 ST9	ST2 ST5 ST6
HANDSHAKE	ST7	

Date	Verify	Modifications	Version
18/10/2005			

Date : 18/10/2005  
Draw by: DD-FAB00  
Check : T.PAYET  
Project: TAMA

PHILIPS SEMICONDUCTORS  
2, Rue de la Cirrofe  
BP 5120  
1409 GEN CDEX 5

TITLE: COMPONENTS IMPLANTATION  
PN531\_HSU

SIZE: 3  
PCB1643-1  
PL: 1/1

