



INTERFACE	STRAPP CONNECTED	STRAPP NOT CONNECTED
HSU	ST5 ST6 ST2 ST9	ST3 ST7 ST8
I2C	ST6 ST2 ST9	ST3 ST5 ST7 ST8
SPI mode 0	ST3 ST5 ST9	ST6 ST7 ST8
SPI mode 1	ST3 ST8 ST9	ST2 ST5 ST6 ST7
SPI mode 2	ST3 ST7 ST9	ST2 ST5 ST6 ST8
SPI mode 3	ST3 ST7 ST8 ST9	ST2 ST5 ST6
HANDSHAKE	ST7	

DATE: 19/10/05	DRAWN BY: T. PAYET	VERIFY: C. DEMOMENT	REVISION: 1.0	PROJECT: TAMA
PHILIPS SEMICONDUCTORS 2, Rue de la Girafe BP5120 14079 CAEN CEDEX 5			TITLE: BOARD PN531_HSU	
ALL RIGHTS ARE RESERVED. REPRODUCTION IN WHOLE OR IN PART IS PROHIBITED WITHOUT THE WRITTEN CONSENT OF THE COPYRIGHT OWNER			SIZE: 4	PCB1643-1
				PL. 1/3