

SOT2021-1 BGA548, ball grid array package, 548 terminals, 0.5 mm pitch, 15 mm x 15 mm x 1.94 mm body 16 May 2019 Package information

Package information

Package summary 1

Terminal position code	B (bottom)
Package type descriptive code	BGA548
Package style descriptive code	BGA (ball grid array)
Mounting method type	S (surface mount)
Issue date	09-05-2019
Manufacturer package code	98ASA01401D

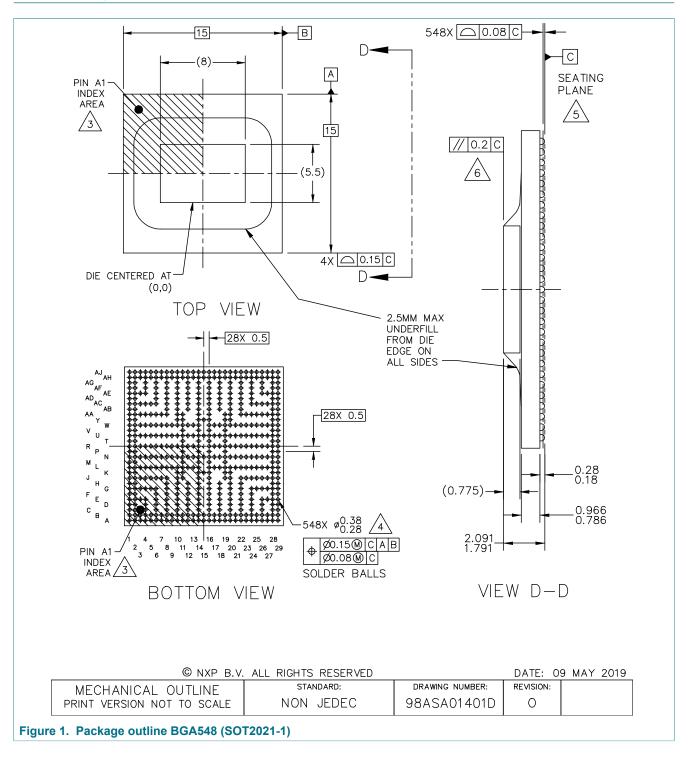
Table 1. Package summary

Parameter	Min	Nom	Мах	Unit
package length	-	15	-	mm
package width	-	15	-	mm
package height	-	1.94	-	mm
nominal pitch	-	0.5	-	mm
actual quantity of termination	-	548	-	



BGA548, ball grid array package, 548 terminals, 0.5 mm pitch, 15 mm x 15 mm x 1.94 mm body

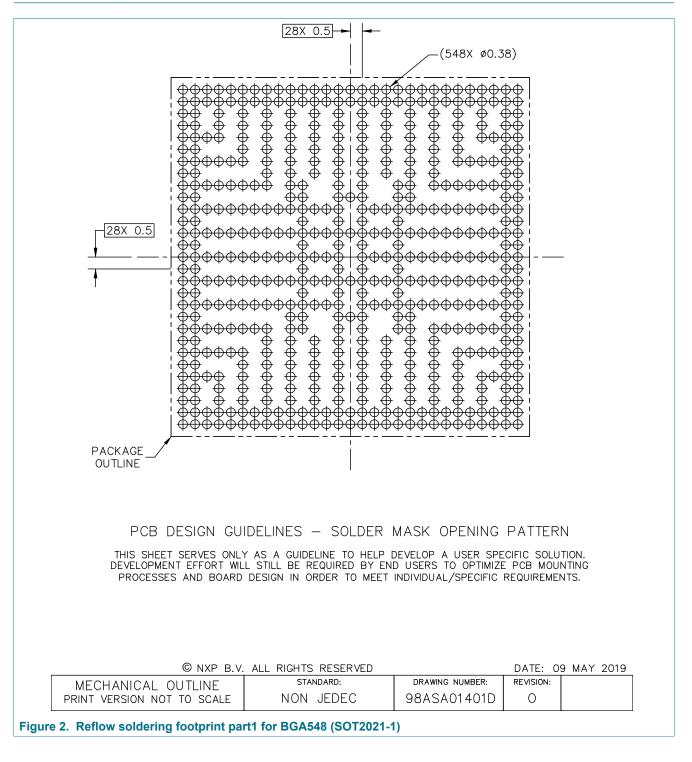
2 Package outline



SOT2021-1
Package information

BGA548, ball grid array package, 548 terminals, 0.5 mm pitch, 15 mm x 15 mm x 1.94 mm body

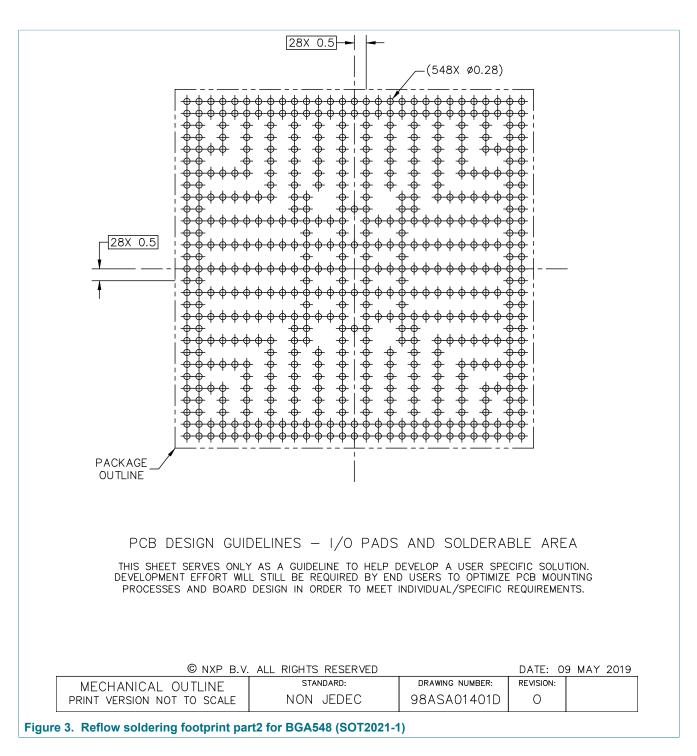
3 Soldering



SOT2021-1
Package information

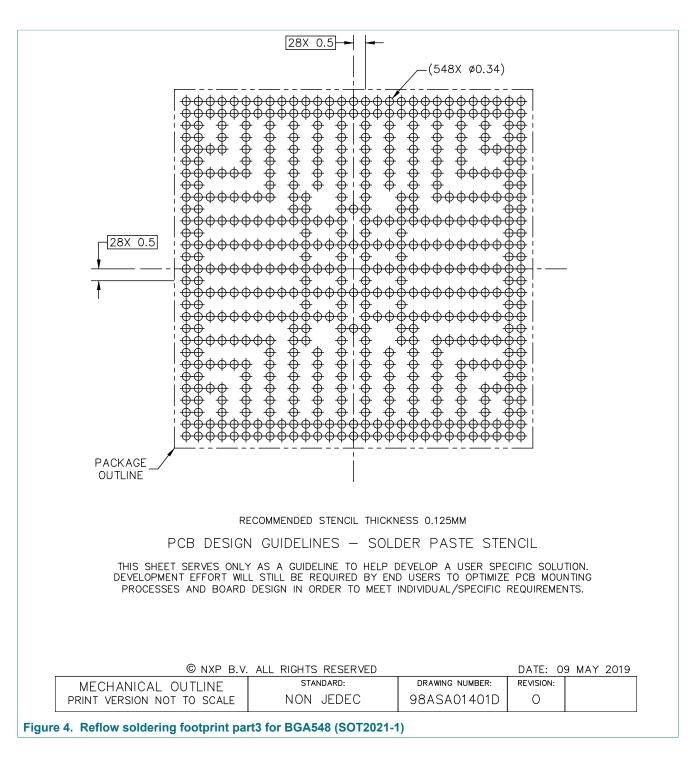
3/8

BGA548, ball grid array package, 548 terminals, 0.5 mm pitch, 15 mm x 15 mm x 1.94 mm body



SOT2021-1
Package information

BGA548, ball grid array package, 548 terminals, 0.5 mm pitch, 15 mm x 15 mm x 1.94 mm body



SOT2021-1
Package information

BGA548, ball grid array package, 548 terminals, 0.5 mm pitch, 15 mm x 15 mm x 1.94 mm body

NOTES:				
1. ALL DIMENSIONS IN MILLIM	ETERS.			
2. DIMENSIONING AND TOLER	ANCING PER ASME Y14.5M-1	994.		
3 PIN A1 FEATURE SHAPE, S	SIZE AND LOCATION MAY VA	RY.		
4. MAXIMUM SOLDER BALL DI	AMETER MEASURED PARALLE	EL TO DATUM C.		
5. DATUM C, THE SEATING P SOLDER BALLS.	LANE, IS DETERMINED BY TH	E SPHERICAL CROWNS	GOF THE	
6. PARALLELISM MEASUREMEN	NT SHALL EXCLUDE ANY EFF	ECT OF MARK ON TOP	P SURFACE	
	ALL RIGHTS RESERVED	1		9 MAY 2019
MECHANICAL OUTLINE PRINT VERSION NOT TO SCALE	standard: NON JEDEC	DRAWING NUMBER: 98ASA01401D	REVISION:	
THE TERSION NOT TO SOALE			0	

SOT2021-1 Package information

NXP Semiconductors

SOT2021-1

BGA548, ball grid array package, 548 terminals, 0.5 mm pitch, 15 mm x 15 mm x 1.94 mm body

4 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

NXP Semiconductors

SOT2021-1

BGA548, ball grid array package, 548 terminals, 0.5 mm pitch, 15 mm x 15 mm x 1.94 mm body

Contents

1	Package summary	1
2	Package outline	2
3	Soldering	
4	Legal information	7
-		

© NXP B.V. 2019.

All rights reserved.

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com Date of release: 16 May 2019