

# SOT1967-1

LFBGA486, low profile fine-pitch ball grid array, 486 balls, 0.5 mm pitch, 14 mm x 14 mm x 1.15 mm body

21 August 2018

Package information

## 1 Package summary

<b>Terminal position code</b>	B (bottom)
<b>Package type descriptive code</b>	LFBGA486
<b>Package style descriptive code</b>	LFBGA (low profile fine-pitch ball grid array)
<b>Mounting method type</b>	S (surface mount)
<b>Issue date</b>	02-08-2018
<b>Manufacturer package code</b>	98ASA01200D

Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	-	14	-	mm
package width	-	14	-	mm
seated height	-	1.15	-	mm
nominal pitch	-	0.5	-	mm
actual quantity of termination	-	486	-	



2 Package outline

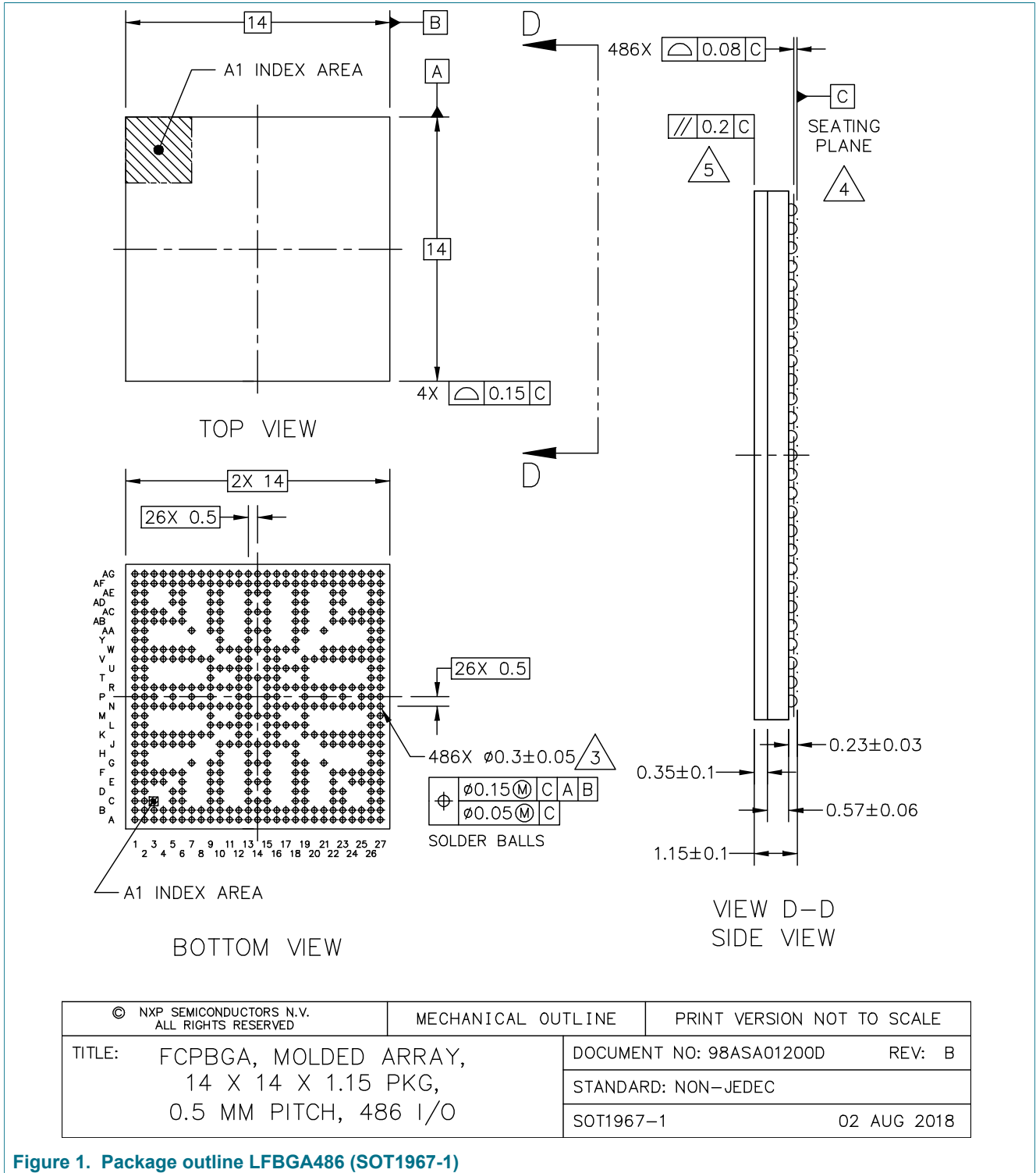


Figure 1. Package outline LFBGA486 (SOT1967-1)

LFBGA486, low profile fine-pitch ball grid array, 486 balls, 0.5 mm pitch, 14 mm x 14 mm x 1.15 mm body

NOTES:

1. ALL DIMENSIONS IN MILLIMETERS.
2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
3. MAXIMUM SOLDER BALL DIAMETER MEASURED PARALLEL TO DATUM C. RAW BALL DIAMETER IS 0.3MM.
4. DATUM C, THE SEATING PLANE, IS DETERMINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.
5. PARALLELISM MEASUREMENT SHALL EXCLUDE ANY EFFECT OF MARK ON TOP SURFACE OF PACKAGE.
6. ALL DIMENSIONS ARE SYMMETRIC ACROSS THE PACKAGE CENTER LINES, UNLESS DIMENSIONED OTHERWISE.

© NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED	MECHANICAL OUTLINE	PRINT VERSION NOT TO SCALE
TITLE: FCPBGA, MOLDED ARRAY, 14 X 14 X 1.15 PKG, 0.5 MM PITCH, 486 I/O	DOCUMENT NO: 98ASA01200D	REV: B
	STANDARD: NON-JEDEC	
	SOT1967-1	02 AUG 2018

Figure 2. Package outline note LFBGA486 (SOT1967-1)

### 3 Legal information

---

#### Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Contents

---

1 Package summary .....1  
2 Package outline .....2  
3 Legal information .....4