

1. Package summary

Terminal position code B (bottom)

Package type descriptive code WLCSP214

Package type industry code WLCSP214

Package style descriptive code WLCSP (wafer level chip-size package)

Mounting method type S (surface mount)

Issue date28-3-2018Manufacturer package code98ASA01053D

Table 1. Package summary

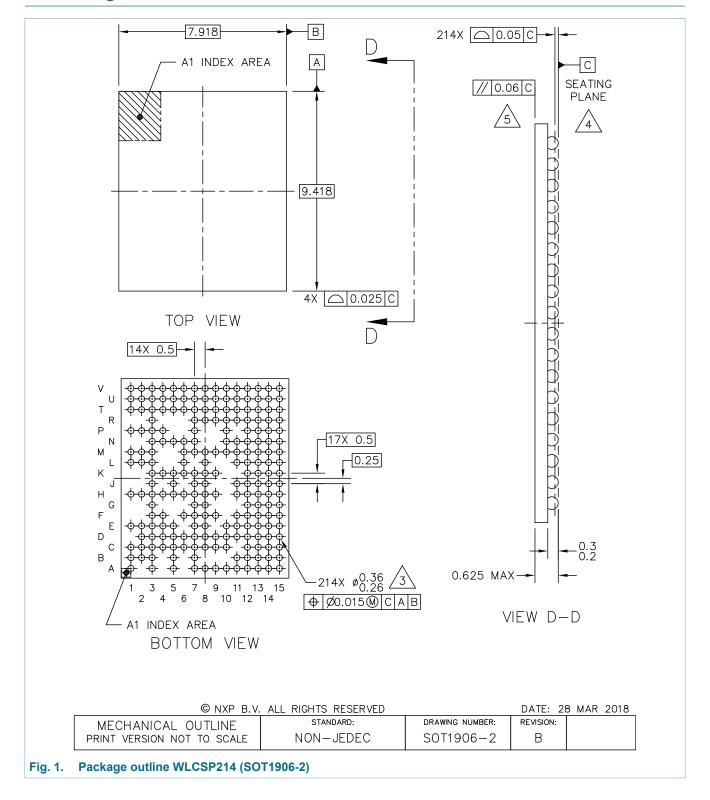
Symbol	Parameter	Min	Тур	Nom	Max	Unit
D	package length	-	-	7.918	-	mm
E	package width	-	-	9.418	-	mm
Α	seated height	-	-	0.55	-	mm
е	nominal pitch	-	-	0.5	-	mm
n ₂	actual quantity of termination	-	-	214	-	A/A



NXP Semiconductors SOT1906-2

WLCSP214, wafer level chip-scale package, 214 terminals, 0.5 mm pitch, 9.418 mm x 7.918 mm x 0.55 mm body

2. Package outline



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WLCSP214, wafer level chip-scale package, 214 terminals, 0.5 mm pitch, 9.418 mm x 7.918 mm x 0.55

NOTES:

1. ALL DIMENSIONS IN MILLIMETERS.

DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

MAXIMUM SOLDER BALL DIAMETER MEASURED PARALLEL TO DATUM C.

DATUM C, THE SEATING PLANE, IS DETERMINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.

PARALLELISM MEASUREMENT SHALL EXCLUDE ANY EFFECT OF MARK ON TOP SURFACE

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DATE: 28 MAR 2018 DRAWING NUMBER: REVISION: STANDARD: MECHANICAL OUTLINE В PRINT VERSION NOT TO SCALE NON-JEDEC SOT1906-2

Package outline note WLCSP214 (SOT1906-2) Fig. 2.

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3. Legal information

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