

SOT1052-2

HXSON8: thermal enhanced extremely thin small outline package; no leads; 8 terminals, 0.5 mm pitch, 3 mm x 2 mm x 0.5 mm body

15 February 2022

Package information

1 Package summary

Terminal position code D (double)

Package type descriptive code HXSON8

Package style descriptive code HXSON (thermal enhanced extremely thin small

outline; no leads)

Package body material type P (plastic)

Mounting method type S (surface mount)

Issue date 06-01-2022

Manufacturer package code 98ASA01831D

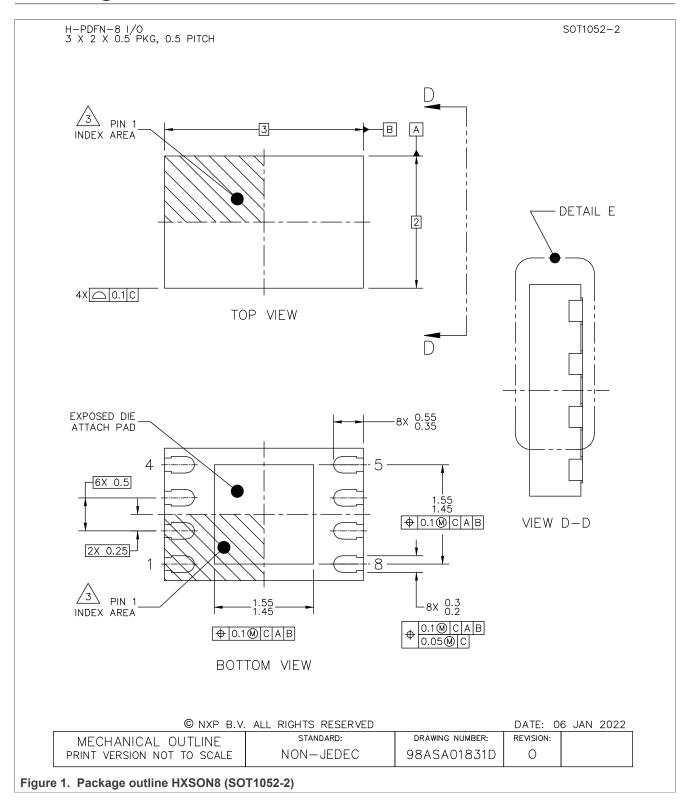
Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	-	3	-	mm
package width	-	2	-	mm
seated height	-	0.5	-	mm

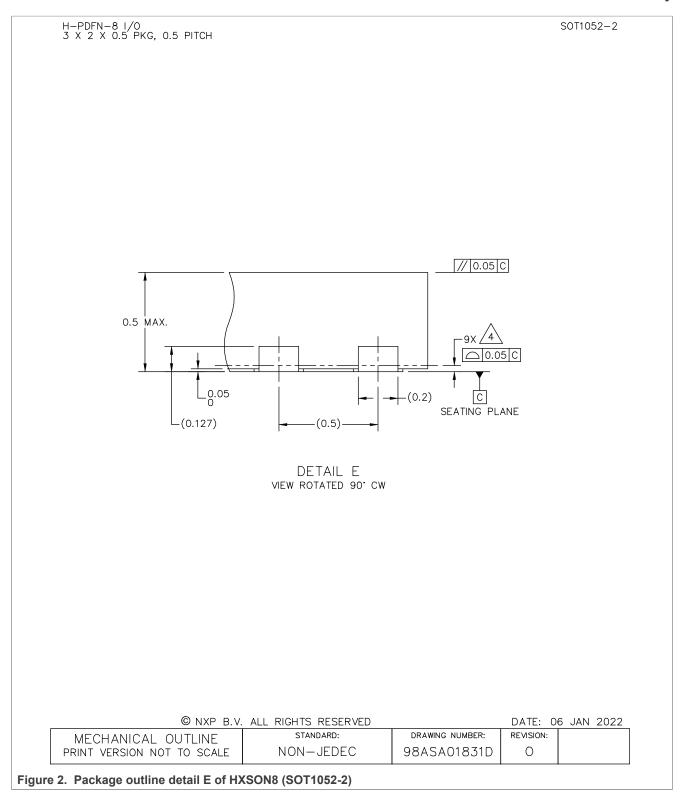


HXSON8: thermal enhanced extremely thin small outline package; no leads; 8 terminals, 0.5 mm pitch, 3 mm x 2 mm x 0.5 mm body

2 Package outline



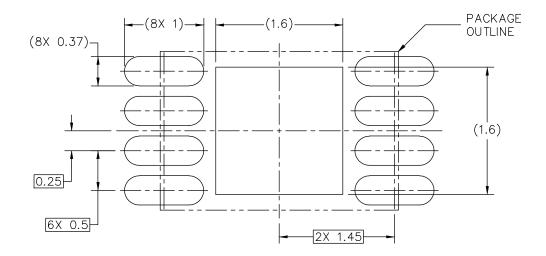
HXSON8: thermal enhanced extremely thin small outline package; no leads; 8 terminals, 0.5 mm pitch, 3 mm x 2 mm x 0.5 mm body



HXSON8: thermal enhanced extremely thin small outline package; no leads; 8 terminals, 0.5 mm pitch, 3 mm x 2 mm x 0.5 mm body

3 Soldering

H-PDFN-8 I/O SOT1052-2 3 X 2 X 0.5 PKG, 0.5 PITCH



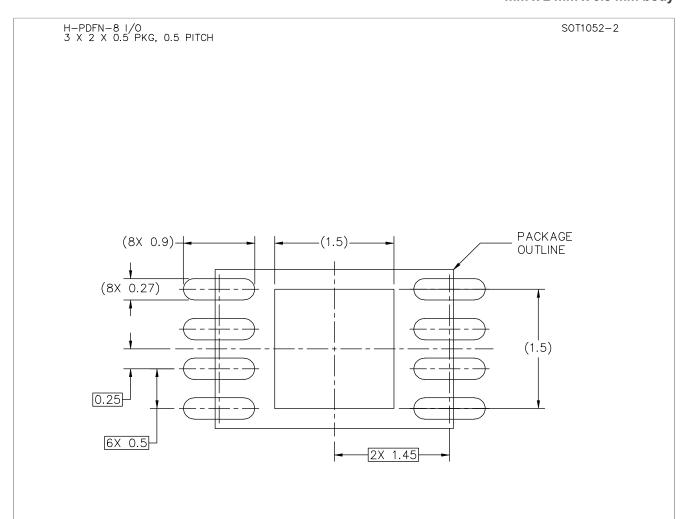
PCB DESIGN GUIDELINES RECOMMENDED SOLDER MASK OPENING PATTERN

THIS SHEET SERVES ONLY AS A GUIDELINE TO HELP DEVELOP A USER SPECIFIC SOLUTION. DEVELOPMENT EFFORT WILL STILL BE REQUIRED BY END USERS TO OPTIMIZE PCB MOUNTING PROCESSES AND BOARD DESIGN IN ORDER TO MEET INDIVIDUAL/SPECIFIC REQUIREMENTS.

© NXP B.V.	ALL RIGHTS RESERVED		DATE: 0	6 JAN 2022
MECHANICAL OUTLINE	STANDARD:	DRAWING NUMBER:	REVISION:	
PRINT VERSION NOT TO SCALE	NON-JEDEC	98ASA01831D	0	

Figure 3. Reflow soldering footprint part1 for HXSON8 (SOT1052-2)

HXSON8: thermal enhanced extremely thin small outline package; no leads; 8 terminals, 0.5 mm pitch, 3 mm x 2 mm x 0.5 mm body



PCB DESIGN GUIDELINES RECOMMENDED I/O PADS AND SOLDERABLE AREA

THIS SHEET SERVES ONLY AS A GUIDELINE TO HELP DEVELOP A USER SPECIFIC SOLUTION. DEVELOPMENT EFFORT WILL STILL BE REQUIRED BY END USERS TO OPTIMIZE PCB MOUNTING PROCESSES AND BOARD DESIGN IN ORDER TO MEET INDIVIDUAL/SPECIFIC REQUIREMENTS.

© NXP B.V.	ALL RIGHTS RESERVED		DATE: 0	6 JAN 2022
MECHANICAL OUTLINE	STANDARD:	DRAWING NUMBER:	REVISION:	
PRINT VERSION NOT TO SCALE	NON-JEDEC	98ASA01831D	0	

Figure 4. Reflow soldering footprint part2 for HXSON8 (SOT1052-2)

HXSON8: thermal enhanced extremely thin small outline package; no leads; 8 terminals, 0.5 mm pitch, 3 mm x 2 mm x 0.5 mm body

H-PDFN-8 I/O 3 X 2 X 0.5 PKG, 0.5 PITCH SOT1052-2 PACKAGE $(8X \ 0.9)-$ —(2X 1.4)— OUTLINE (8X 0.27) $(2X^{'}0.5)$ 2X 0.45 6X 0.5 2X 1.45

RECOMMENDED STENCIL THICKNESS 0.125

PCB DESIGN GUIDELINES - RECOMMENDED SOLDER PASTE STENCIL

THIS SHEET SERVES ONLY AS A GUIDELINE TO HELP DEVELOP A USER SPECIFIC SOLUTION. DEVELOPMENT EFFORT WILL STILL BE REQUIRED BY END USERS TO OPTIMIZE PCB MOUNTING PROCESSES AND BOARD DESIGN IN ORDER TO MEET INDIVIDUAL/SPECIFIC REQUIREMENTS.

© NXP B.V.	ALL RIGHTS RESERVED		DATE: 0	6 JAN 2022
MECHANICAL OUTLINE	STANDARD:	DRAWING NUMBER:	REVISION:	
PRINT VERSION NOT TO SCALE	NON-JEDEC	98ASA01831D	0	

Figure 5. Reflow soldering footprint part3 for HXSON8 (SOT1052-2)

HXSON8: thermal enhanced extremely thin small outline package; no leads; 8 terminals, 0.5 mm pitch, 3 mm x 2 mm x 0.5 mm body H-PDFN-8 I/O 3 X 2 X 0.5 PKG, 0.5 PITCH SOT1052-2 NOTES: 1. ALL DIMENSIONS ARE IN MILLIMETERS. 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994. /3. PIN 1 FEATURE SHAPE, SIZE AND LOCATION MAY VARY. 4. COPLANARITY APPLIES TO LEADS, DIE ATTACH FLAG. 5. MIN. METAL GAP FOR LEAD TO EXPOSED PAD SHALL BE 0.2 MM.

© NXP B.V. ALL RIGHTS RESERVED DATE: 06 JAN 2022 STANDARD: DRAWING NUMBER: REVISION: MECHANICAL OUTLINE

98ASA01831D

0

Figure 6. Package outline note HXSON8 (SOT1052-2)

PRINT VERSION NOT TO SCALE

NON-JEDEC

HXSON8: thermal enhanced extremely thin small outline package; no leads; 8 terminals, 0.5 mm pitch, 3 mm x 2 mm x 0.5 mm body

4 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including -without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

HXSON8: thermal enhanced extremely thin small outline package; no leads; 8 terminals, 0.5 mm pitch, 3 mm x 2 mm x 0.5 mm body

Contents

1	Package summary	1
2	Package outline	
3	Soldering	
4	-	