# AN14335

# PN7220 Frequently asked questions Rev. 1.0 — 29 August 2024

**Application note** 

#### **Document information**

Information	Content
Keywords	PN7220, EMVCo, NFC Forum, Android
Abstract	This document covers frequently asked questions about PN7220 in a question-answer style.



PN7220 Frequently asked questions

#### 1 Introduction

This document is a collection of frequently asked questions about PN7220.

For more information, refer to the PN7220 product page [1] and related documentation (see Section 13).

#### 2 How to build GSI image with PN7220 MW?

When using a GSI image, users need to include all MW changes introduced by NXP into this specific build. In case of any differences, the user must correct them. See [2] for information on how to include NXP changes into AOSP code. The same (with possible changes) is needed also with GSI image.

# 3 What to do when the following error is reported: Ignore shared library com.nxp.nfc: /system/framework/com.nxp.nfc.jar does not exist

If MW is reporting the error Ignore shared library com.nxp.nfc: /system/framework/com.nxp.nfc.jar does not exist, check if com.nxp.nfc.jar exists in the out/target/product/name\_of\_board/system/framework/ folder. If not, follow the steps found in [2] chapter 3, subchapter 3.1 (see step 8 for explanation).

# 4 What to do when the following error is reported: Ignore shared library com.nxp.emvco.jar: /system/framework/ com.nxp.emvco.jar does not exist

If MW is reporting the error Ignore shared library com.nxp.emvco.jar: /system/framework/ com.nxp.emvco.jar does not exist, check if com.nxp.emvco.jar exists in out/target/product/name\_of\_board/system/framework/ folder. If not, follow the steps found in [2] chapter 3, subchapter 3.1 (see step 8 for explanation).

## 5 What to do when VTS and CTS tests are failing?

When VTS/CTS tests fail, report the failure to NXP by contacting your CAS contact person. Provide the following information:

- · the test case that led to the failure,
- · debug log of the test case.

Make sure that debug logs are enabled to record information about the failure.

To enable logs, set the flags in specific configuration files to following values:

#### libnfc-nci.conf NFC\_DEBUG\_ENABLED=0x01

# libnfc-nxp.conf NXPLOG\_EXTNS\_LOGLEVEL=0x03 NXPLOG\_NCIHAL\_LOGLEVEL=0x03 NXPLOG\_NCIX\_LOGLEVEL=0x03 NXPLOG\_NCIR\_LOGLEVEL=0x03 NXPLOG\_FWDNLD\_LOGLEVEL=0x03 NXPLOG\_TML\_LOGLEVEL=0x03

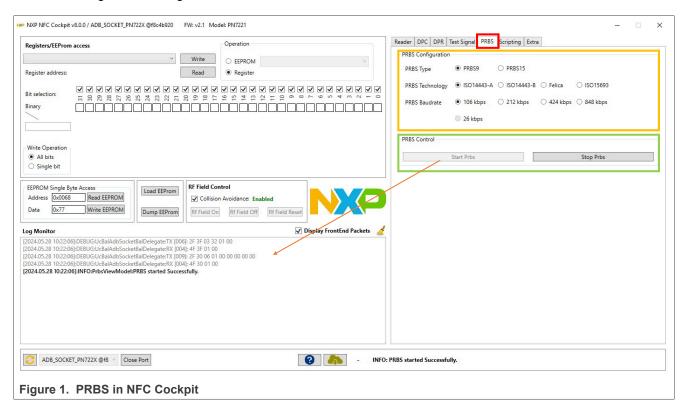
AN14335

PN7220 Frequently asked questions

## 6 How to get PRBS NCI commands?

First, refer to the user manual [4]. Since PN7220 also supports the NFC Cockpit [5], user can get different NCI commands (also for RF settings) inside the Cockpit tool. See [6] for installation instructions. See Figure 1 on how to get the NCI commands for PRBS. Follow the steps below:

- 1. Select "PRBS" tab (red square)
- 2. Configure PRBS (orange square)
- 3. Start/Stop PRBS (green square)
- 4. Check "Log Monitor" to get the NCI commands



## 7 Why does the FW update not work?

NXP supports two versions of PN7220: (check [6] for more details)

- · Single-Host
- Dual-Host

In Dual-Host mode, FW update is possible only via Secure MCU and not Android host.

In Single host mode, FW update is possible from Android host. See [6], chapter "Firmware update" for detailed information on requirements and instructions for performing the FW update.

PN7220 Frequently asked questions

# 8 There is no communication between PN7220 and Android host. What can I do?

First, check the *adb logcat* logs. Inside the logs, users should search for driver probing. If this action was successful, a log as shown in Figure 2 is outputted (specific information in the resulting log may differ):

```
nfc_i2c_dev_init: Loading NXP NFC I2C driver
nfc_i2c_dev_probe: i2c slave address 0x28
nfc_parse_dt: irq 86
nfc_parse_dt: 86, 84, 87, 85, 140, 141, 138, 139
nfc_i2c_dev_probe: requesting IRQ 152
nfc_i2c_dev_probe: requesting IRQ 207
nfc_i2c_dev_probe: probing nfc_i2c successfully
init : Loaded kernel module /lib/modules/nxpnfc-i2c.ko

Figure 2. Driver probing
```

Following this, ensure that all physical connections are correct. The easiest way is to use a logic analyzer. If the actions presented in this chapter do not bring any success, contact NXP and send the logs (including Android bootup and debug level enabled) for an analysis.

# 9 What happens with the VEN pin, when the NFC button is toggled in Android?

In Android, users can enable or disable NFC with a button:



By default, the VEN pin goes to low when users disable NFC in Android (see <u>Figure 4</u>). When NFC is enabled, the VEN pin goes to high again (see <u>Figure 5</u>). If this is not the case within the customer environment, then either, changes were made by the user, or integration was not performed correctly. If the cause is incorrect integration, refer to [2] for a step-by-step guide on how to correctly integrate NXP changes into AOSP code.

#### PN7220 Frequently asked questions

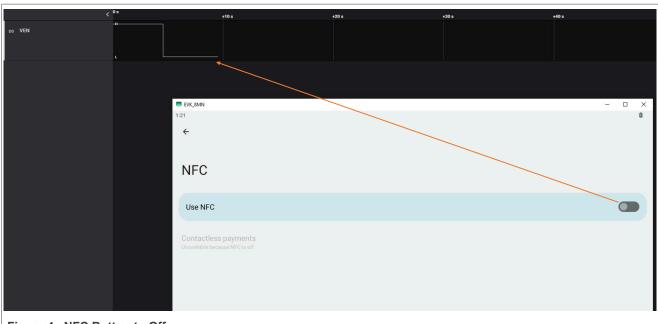


Figure 4. NFC Button to Off



PN7220 Frequently asked questions

# 10 I cannot use the NFC Button in Android since it is disabled. What can I do?

See [2] and ensure that all steps were followed correctly. In most cases, this issue is caused by incorrect integration of changes provided by NXP.

#### 11 There is an RF field constantly ON or OFF, what do I need to do?

Provide NXP with a detailed description on how **the issue can be reproduced** and logs from bootup until issue observed if possible. If this is a long-running test, provide two separate logs:

- 1. bootup logs
- 2. issue observation

If the issue does not occur every time, provide logs of both successful and unsuccessful test cases recorded by the same card.

It is important that the flags inside the configuration files are configured as follows:

#### Inside libnfc-nxp.conf:

```
NXPLOG_EXTNS_LOGLEVEL=0x03

NXPLOG_NCIHAL_LOGLEVEL=0x03

NXPLOG_NCIX_LOGLEVEL=0x03

NXPLOG_NCIR_LOGLEVEL=0x03

NXPLOG_FWDNLD_LOGLEVEL=0x03

NXPLOG_TML_LOGLEVEL=0x03
```

#### Inside libnfc-nci.conf:

```
NFC_DEBUG_ENABLED=0x01
```

#### Inside libemvco-nxp.conf:

```
NXP_LOG_EXTNS_LOGLEVEL=0x03

NXP_LOG_NCIHAL_LOGLEVEL=0x03

NXP_LOG_NCIX_LOGLEVEL=0x03

NXP_LOG_NCIR_LOGLEVEL=0x03

NXP_LOG_TML_LOGLEVEL=0x03

NXP_EMVCO_DEBUG_ENABLED=0x03
```

If RF logs are available, users can also share them with NXP for analysis of the issue.

PN7220 Frequently asked questions

## 12 How to link a register address to an NCI command

In the PN7220 data sheet [3], settings are described with their corresponding EEPROM address. For example, as shown in Figure 6 below:

#### 9.18.2.3.1 DPC\_CONFIG (0x0068)

**DPC** configuration

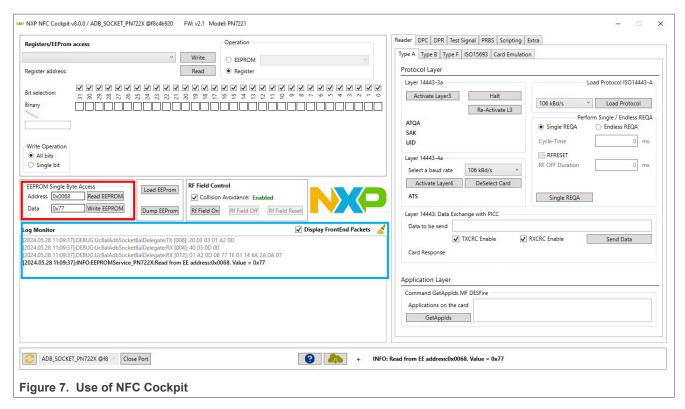
Table 141. DPC\_CONFIG (0x0068)

Function	bit	Values	Description
RFU	[7:3]		RFU
DPC in Active target mode	[2]		DPC configuration in active target mode
		0x00	disabled
		0×01	enabled
DPC in Active initiator mode	[1]		DPC configuration in active initiator mode
		0x00	disabled
		0×01	enabled
DPC in Reader/Passive Initiator mode	[0]		DPC configuration in Reader/ Passive Initiator mode
		0x00	disabled
		0x01	enabled

Figure 6. Example from data sheet

Users can use the NFC Cockpit to read and write to specific addresses via the EEPROM Single-Byte Access (see red square in <a href="Figure 7">Figure 7</a> below). To read, enter the address from the data sheet into the "Address" field, and then click Read EEPROM. To "Write", input the content into the "Data" field, and click "Write EEPROM. If "Display FrontEnd Packets" is enabled, users can also check the NCI commands (see blue square in <a href="Figure 7">Figure 7</a> below).

#### PN7220 Frequently asked questions



In the Android environment, users need to use the configuration file to link a register address to an NCI command. To map addresses to an NCI tag, the following approach can be used:

Each NCI tag can store 8 bytes of data:

```
0x0000 - 0x0007 = A2 00
0x0008 - 0x000F = A2 01
...
0x0068 - 0x006F = A2 0D
...
```

To read values, issue the NCI Core Get CMD command as follows:

```
20 03 03 01 A2 0D
```

#### Command output example:

```
40 03 0D 00 01 A2 0D 08 77 1E 01 14 6A 2A 0A 07:
77 => 0x0068
1E => 0x0069
01 => 0x006A
14 => 0x006B
6A => 0x006C
2A => 0x006C
0A => 0x006E
07 => 0x006F
```

#### PN7220 Frequently asked questions

To write to a specific NCI tag, issue the following command:

```
20 02 0C 01 A2 0D 08 77 1E 01 14 6A 2A 0A 07 // This command will write all 8 bytes 20 02 05 01 A2 0D 01 77 // This command is only for 0x0068
```

To write to 0x006A, the user cannot only write to this single byte/tag. Users must also write to all bytes before 0x006A. In this example:

- 0x0068
- 0x0069
- 0x006A

20 02 07 01 A2 0D 03 77 1E 01 // This command will write only the first three bytes

0x006F can only be changed by issuing the following command:

20 02 0C 01 A2 0D 08 77 1E 01 14 6A 2A 0A 07 // This command will write all 8 bytes, even if we want to change only lasy byte of the tag.

#### PN7220 Frequently asked questions

### 13 References

- [1] Webpage PN7220 High-Performance, One-Chip NFC Controller for EMVCo 3.1 and NFC Forum Operation(link)
- [2] Application note AN13971 PN7220 & PN7160 Android 13 porting guide (link)
- [3] Data sheet PN7220 NFC controller with NCI interface supporting EMV and NFC Forum applications (link)
- [4] User manual UM11810 PN722X NFC controller (link)
- [5] Software NFC Cockpit Configuration Tool for NFC ICs (link)
- [6] User guide UG10068 PN7220 Quick start guide (link)

PN7220 Frequently asked questions

#### 14 Note about the source code in the document

The example code shown in this document has the following copyright and BSD-3-Clause license:

Copyright 2024 NXP Redistribution and use in source and binary forms, with or without modification, are permitted provided that the following conditions are met:

- 1. Redistributions of source code must retain the above copyright notice, this list of conditions and the following disclaimer.
- 2. Redistributions in binary form must reproduce the above copyright notice, this list of conditions and the following disclaimer in the documentation and/or other materials must be provided with the distribution.
- 3. Neither the name of the copyright holder nor the names of its contributors may be used to endorse or promote products derived from this software without specific prior written permission.

THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUTORS "AS IS" AND ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE ARE DISCLAIMED. IN NO EVENT SHALL THE COPYRIGHT HOLDER OR CONTRIBUTORS BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.

PN7220 Frequently asked questions

## 15 Revision history

#### Table 1. Revision history

Document ID	Release date	Description
AN14335 v.1.0	29 August 2024	Initial version

#### PN7220 Frequently asked questions

## **Legal information**

#### **Definitions**

**Draft** — A draft status on a document indicates that the content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included in a draft version of a document and shall have no liability for the consequences of use of such information.

#### **Disclaimers**

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at https://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Suitability for use in non-automotive qualified products — Unless this document expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

**HTML publications** — An HTML version, if available, of this document is provided as a courtesy. Definitive information is contained in the applicable document in PDF format. If there is a discrepancy between the HTML document and the PDF document, the PDF document has priority.

**Translations** — A non-English (translated) version of a document, including the legal information in that document, is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Security — Customer understands that all NXP products may be subject to unidentified vulnerabilities or may support established security standards or specifications with known limitations. Customer is responsible for the design and operation of its applications and products throughout their lifecycles to reduce the effect of these vulnerabilities on customer's applications and products. Customer's responsibility also extends to other open and/or proprietary technologies supported by NXP products for use in customer's applications. NXP accepts no liability for any vulnerability. Customer should regularly check security updates from NXP and follow up appropriately. Customer shall select products with security features that best meet rules, regulations, and standards of the intended application and make the ultimate design decisions regarding its products and is solely responsible for compliance with all legal, regulatory, and security related requirements concerning its products, regardless of any information or support that may be provided by NXP.

NXP has a Product Security Incident Response Team (PSIRT) (reachable at <a href="PSIRT@nxp.com">PSIRT@nxp.com</a>) that manages the investigation, reporting, and solution release to security vulnerabilities of NXP products.

 $\ensuremath{\mathsf{NXP}}\xspace\,\ensuremath{\mathsf{B.V.}}\xspace - \ensuremath{\mathsf{NXP}}\xspace\,\ensuremath{\mathsf{B.V.}}\xspace$  is not an operating company and it does not distribute or sell products.

#### PN7220 Frequently asked questions

#### Licenses

Purchase of NXP ICs with NFC technology — Purchase of an NXP Semiconductors IC that complies with one of the Near Field Communication (NFC) standards ISO/IEC 18092 and ISO/IEC 21481 does not convey an implied license under any patent right infringed by implementation of any of those standards. Purchase of NXP Semiconductors IC does not include a license to any NXP patent (or other IP right) covering combinations of those products with other products, whether hardware or software.

#### **Trademarks**

Notice: All referenced brands, product names, service names, and trademarks are the property of their respective owners.

NXP — wordmark and logo are trademarks of NXP B.V.

#### PN7220 Frequently asked questions

Tab. 1. Revision history ......12

## **Figures**

Fig. 1.	PRBS in NFC Cockpit3	Fig. 5.	NFC Button to On5
Fig. 2.	Driver probing4	Fig. 6.	Example from data sheet7
Fig. 3.	NFC Button in Android4	Fig. 7.	Use of NFC Cockpit8
Fig 4	NEC Button to Off 5		

#### PN7220 Frequently asked questions

#### **Contents**

1	Introduction	2
2	How to build GSI image with PN7220	
	MW?	2
3	What to do when the following	
	error is reported: Ignore shared	
	library com.nxp.nfc: /system/framework/	
	com.nxp.nfc.jar does not exist	2
4	What to do when the	
	following error is reported: Ignore	
	shared library com.nxp.emvco.jar: /	
	system/framework/ com.nxp.emvco.jar	
	does not exist	2
5	What to do when VTS and CTS tests are	
	failing?	2
6	How to get PRBS NCI commands?	3
7	Why does the FW update not work?	3
8	There is no communication between	
	PN7220 and Android host. What can I	
	do?	4
9	What happens with the VEN pin, when	
	the NFC button is toggled in Android?	4
10	I cannot use the NFC Button in Android	
	since it is disabled. What can I do?	6
11	There is an RF field constantly ON or	
	OFF, what do I need to do?	6
12	How to link a register address to an NCI	
	command	7
13	References	10
14	Note about the source code in the	
	document	
15	Revision history	
	Legal information	13

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.