AN14273 MCX N23x Estimated Power-on Hours Rev. 1 – 20 March 2024

Application note

Document information

Information	Content
Keywords	AN14273, MCX, MCX N, Lifetime, Power-on hours, temperature, PoH
Abstract	This document describes the estimated product Power-on Hours (PoH) for the MCX N23x (devices) based on the criteria used in the qualification process.



AN14273

1 Introduction

This document describes the estimated product Power-on Hours (PoH) for the MCX N23x (devices) based on the criteria used in the qualification process.

The product PoH described here are estimates and do not represent a guaranteed lifetime for a product.

This document provides guidance on how to interpret the different device qualification levels in terms of the target operating voltage, the maximum supported junction temperature (T_j) , and how these relate to the PoH of the device.

The data presented in this document is provided for convenience. However, it does not represent all potential failing mechanisms and may not accurately represent behavior for all mission profiles or applications. The data is based on a single activation energy and voltage acceleration parameter, using the Arrhenius equation for temperature acceleration and Power Law for voltage acceleration, along with the data collected during High Temperature Operating Life (HTOL), to demonstrate how temperature could impact the PoH of the product.

2 Device qualification level and available PoH

Each qualification level supported (Industrial) defines several Power-on Hours (PoH) available to the device under a given set of conditions such as:

- The target core voltage (VDD_CORE) for the application (Industrial).
- The junction temperature of the device (T_i).
 - While the device can operate at the maximum T_j listed in its data sheet, operating the device at this temperature for an extended period reduces its operating PoH.
 - Make sure that the device is appropriately thermally managed and the maximum junction temperature is not exceeded.

The junction temperature (T_j) of the device is the temperature of the transistors in the device. It is a different measurement than the case and the ambient temperature. Most applications do not have a constant T_j during operation.

The charts in this document show the relationship between the T_j and Power-on Hours. The percentage of ontime at different temperatures is part of what defines each Mission Profile. When the junction temperature is not constant during the customer application, the Effective Junction Temperature (T_{j-eff}) can be calculated using weighting with the Arrhenius factor (for more about T_{j-eff} , see <u>Section 3</u>).

Note: Data provided in this document are estimates for PoH based on qualification test data and experience with this product. These estimates must not be viewed as a limit on an individual device lifetime. They must not be construed as a guarantee by NXP as to the actual lifetime of the device. Sales and warranty terms and conditions still apply.

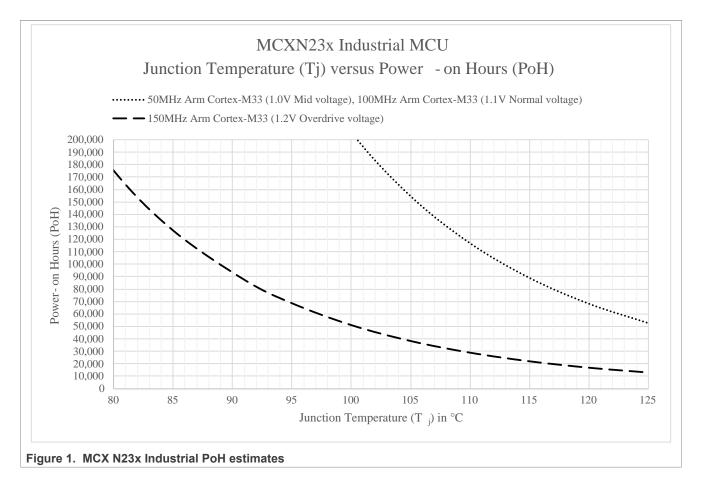
2.1 Industrial qualification

<u>Figure 1</u> provides the number of PoH for the use conditions of the Industrial device. The PoH value assumes that the product is powered on and active for 100 % of the time (100 % duty cycle). PoH can be read directly from the curves in <u>Figure 1</u> to determine the impact of junction temperature at the listed conditions.

NXP Semiconductors

AN14273

MCX N23x Estimated Power-on Hours



3 Effective junction temperature

The junction temperature (T_j) of the device is the temperature of the transistors in the device. It is a different measurement than the case and the ambient temperature. Most applications do not have a constant T_j during operation.

The charts in this document show the relationship between the T_j and Power-on Hours. The percentage of ontime at different temperatures is part of what defines each Mission Profile. The Effective Junction Temperature (T_{j-eff}) is the single T_j that represents the mission profile and can be used to extrapolate the PoH in the charts above.

- The T_{j-eff} depends only on the temperatures during the on-time duty cycles of a Mission Profile. Temperatures
 when the device is powered off do not affect T_{j-eff}.
- T_{j-eff} is not a simple average of temperatures, as the on-time at higher temperatures consumes more operating life than on-time at lower temperatures.
- When the junction temperature is not constant during the customer application, the T_{j-eff} can be calculated using weighting with the Arrhenius factor.

3.1 Calculating T_{j-eff}

Assuming that the temperature dependence follows Arrhenius behavior, one can calculate the T_{j-eff} using the following method:

1. Determine the percentage of time (t_n) that the application will be powered on at a small set of discrete temperatures (T_n).

AN14273

MCX N23x Estimated Power-on Hours

2. Calculate the average failure rate using the Arrhenius method.

$$FR_{AV} = \left[t_1 \cdot e^{\frac{-E_A}{kT_1}} + t_2 \cdot e^{\frac{-E_A}{kT_2}} + \dots + t_n \cdot e^{\frac{-E_A}{kT_n}} \right]$$
(1)

3. The effective temperature can then be calculated.

$$\Gamma_{j-eff} = \frac{-E_A}{k \ln(FR_{AV})} \tag{2}$$

Here are some notes on the variables and constants used in the formulas above:

E_A = Activation Energy. A typical value is 0.7 eV and is what is used to generate the charts in this document.

k = Boltzmann constant. 8.62 X 10⁻⁵

 T_n = The temperature must be noted in Kelvin. The end result for T_{i-eff} is also in Kelvin.

 t_n = The percentage of time at a given temperature is in decimal. For instance, 50 % is 0.50.

Here is a simple example calculating the T_{j-eff} of an application that has two different constant values. In this scenario, the T_j of the device is at 100 °C for 50 % of the time, and 50 °C for the other 50 % of time the device is powered on, resulting in an average of 75 °C.

$$FR_{AV} = \left[0.5 \cdot e^{\frac{-0.7}{k_373.15}} + 0.5 \cdot e^{\frac{-0.7}{k_323.15}} \right] = 1.83 \times 10^{-10}$$
(3)

$$T_{j\text{-}eff} = \frac{-0.7}{k \ln(FR_{AV})} = 362.18 \ K = 89.03^{\circ}C$$
⁽⁴⁾

In this example you can see that the T_{j-eff} of 89 °C is much higher than the average temperature of 75 °C, showing that higher temperatures have a bigger impact on the life of the device.

4 Conclusion

Selecting the optimal operating performance point and thermal envelope is critical to meet the target application PoH. Trade-offs between the target operating voltage/frequency of the device and the operating junction temperature (Tj) of the device can greatly improve the PoH of the device.

Lowering the operating junction temperature in the application is the most effective means to increase the PoH of the device without affecting the performance of the device. To accomplish it, increase the thermal dissipation capacity in the application. In cases where the thermal properties cannot be altered, a lower operating voltage can be used to increase the PoH of the device. Lowering the voltage may result in lower performance; the operating frequency may have to be adjusted lower to match the voltage specified in the data sheet.

The data and examples provided in this application are a reference to support the customer in their application development.

5 Revision history

Table 1 summarizes the revisions to this document.

Table 1. Revision history

Document ID	Release date	Description
AN14273 v.1	20 March 2024	Initial public release

AN14273

MCX N23x Estimated Power-on Hours

Legal information

Definitions

Draft — A draft status on a document indicates that the content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included in a draft version of a document and shall have no liability for the consequences of use of such information.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at https://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Suitability for use in non-automotive qualified products — Unless this document expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

Translations — A non-English (translated) version of a document, including the legal information in that document, is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Security — Customer understands that all NXP products may be subject to unidentified vulnerabilities or may support established security standards or specifications with known limitations. Customer is responsible for the design and operation of its applications and products throughout their lifecycles to reduce the effect of these vulnerabilities on customer's applications and products. Customer's responsibility also extends to other open and/or proprietary technologies supported by NXP products for use in customer's applications. NXP accepts no liability for any vulnerability. Customer should regularly check security updates from NXP and follow up appropriately. Customer shall select products with security features that best meet rules, regulations, and standards of the intended application and make the ultimate design decisions regarding its products and is solely responsible for compliance with all legal, regulatory, and security related requirements concerning its products, regardless of any information or support that may be provided by NXP.

NXP has a Product Security Incident Response Team (PSIRT) (reachable at <u>PSIRT@nxp.com</u>) that manages the investigation, reporting, and solution release to security vulnerabilities of NXP products.

 $\ensuremath{\mathsf{NXP}}\xspace \mathsf{B.V.}$ — NXP B.V. is not an operating company and it does not distribute or sell products.

Trademarks

Notice: All referenced brands, product names, service names, and trademarks are the property of their respective owners. **NXP** — wordmark and logo are trademarks of NXP B.V.

MCX — is a trademark of NXP B V

AN14273

MCX N23x Estimated Power-on Hours

Contents

1	Introduction	2
2	Device qualification level and available	
	РоН	2
2.1	Industrial qualification	2
3	Effective junction temperature	3
3.1	Calculating Tj-eff	3
4	Conclusion	4
5	Revision history	4
	Legal information	

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© 2024 NXP B.V.

All rights reserved.

For more information, please visit: https://www.nxp.com

Date of release: 20 March 2024 Document identifier: AN14273