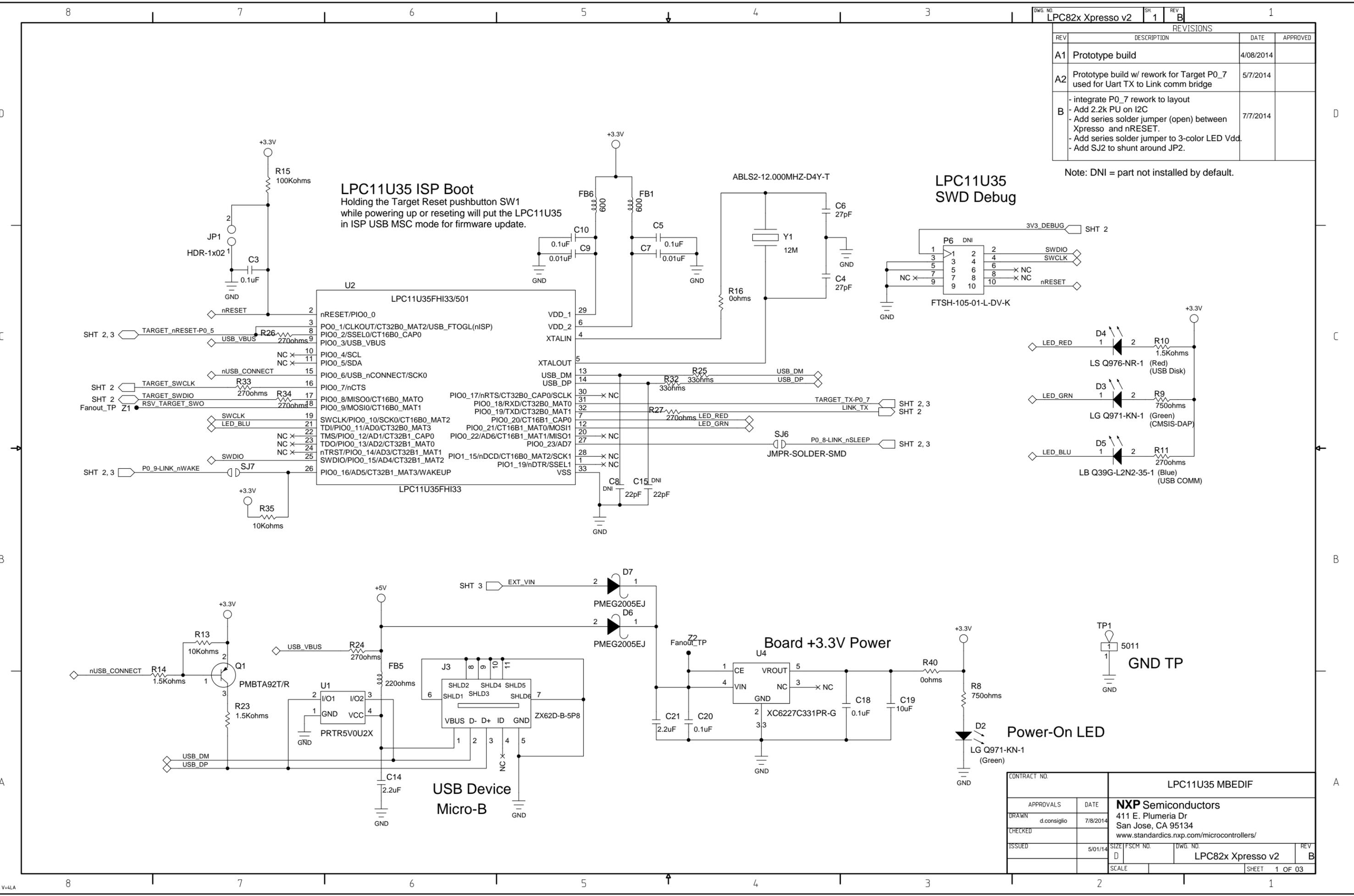


| REVISIONS | | | |
|-----------|--|-----------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| A1 | Prototype build | 4/08/2014 | |
| A2 | Prototype build w/ rework for Target P0_7 used for Uart TX to Link comm bridge | 5/7/2014 | |
| B | - integrate P0_7 rework to layout - Add 2.2k PU on I2C - Add series solder jumper (open) between Xpresso and nRESET. - Add series solder jumper to 3-color LED Vdd. - Add SJ2 to shunt around JP2. | 7/7/2014 | |

Note: DNI = part not installed by default.



LPC11U35 ISP Boot
 Holding the Target Reset pushbutton SW1 while powering up or resetting will put the LPC11U35 in ISP USB MSC mode for firmware update.

LPC11U35 SWD Debug

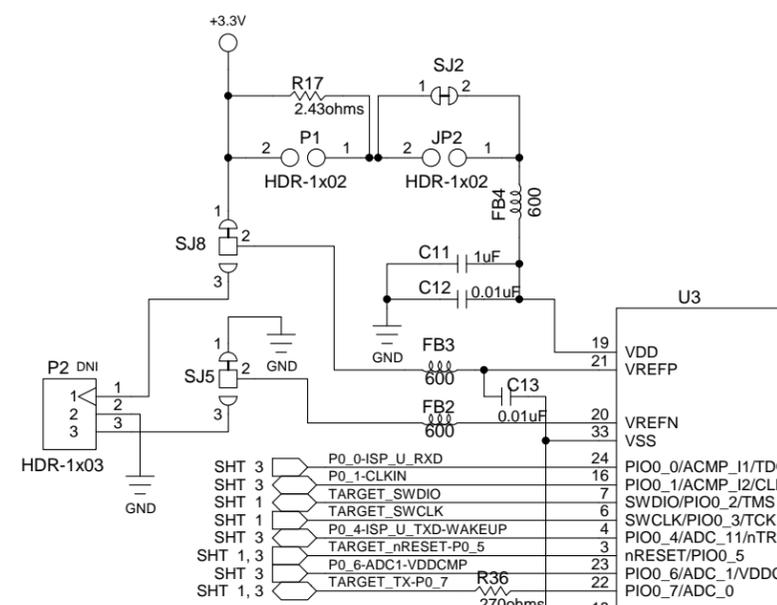
Board +3.3V Power

USB Device Micro-B

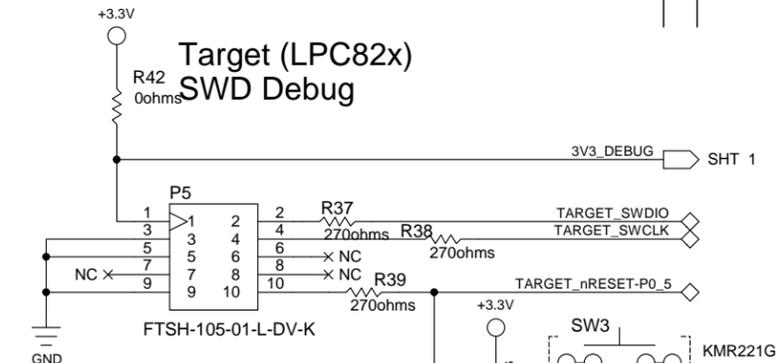
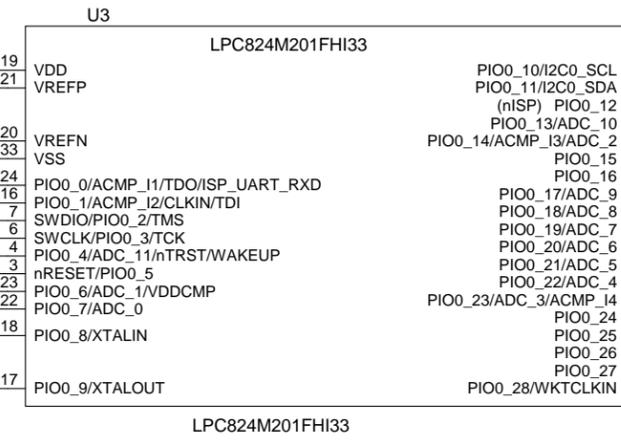
Power-On LED

| | | | |
|-------------------|----------|---|-------------------|
| CONTRACT NO. | | LPC11U35 MBEDIF | |
| APPROVALS | DATE | NXP Semiconductors | |
| DRAWN d.consiglio | 7/8/2014 | 411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/ | |
| CHECKED | | SIZE | DWG. NO. |
| ISSUED | 5/01/14 | D | LPC82x Xpresso v2 |
| | | SCALE | REV B |
| | | | SHEET 1 OF 03 |

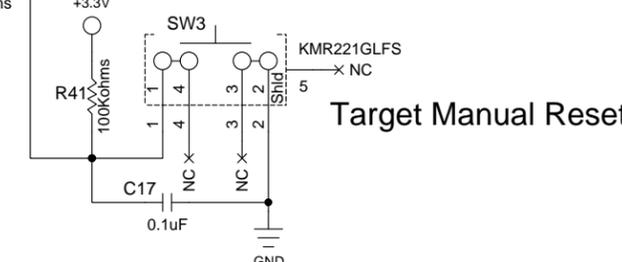
| REVISIONS | | | |
|-----------|-------------|------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |



Target LPC82x

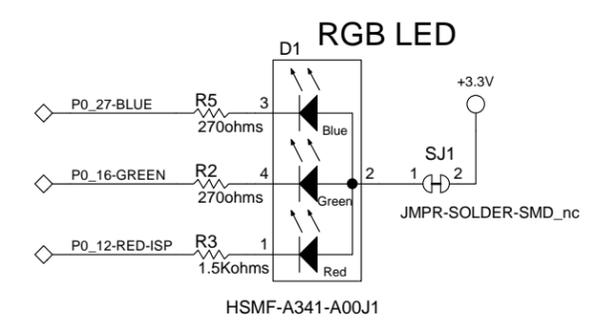
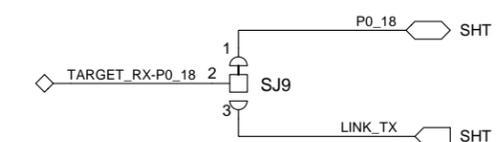
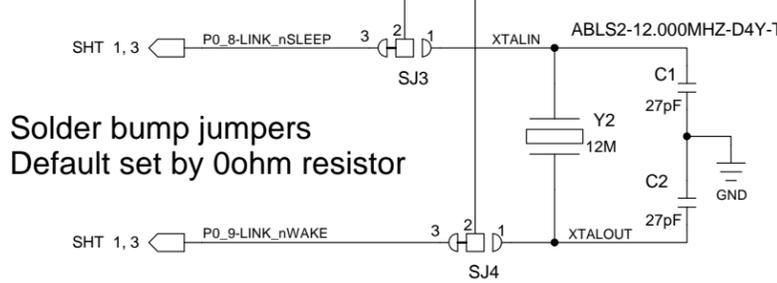


Target (LPC82x) SWD Debug



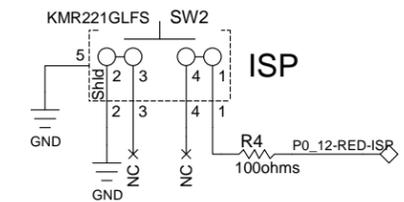
Target Manual Reset

Solder bump jumpers
Default set by 0ohm resistor

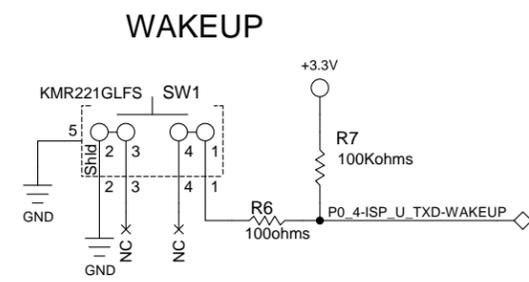


RGB LED

Holding the Target ISP pushbutton SW2 while powering up or resetting will put the LPC82x in ISP USB MSC mode for firmware update.



ISP

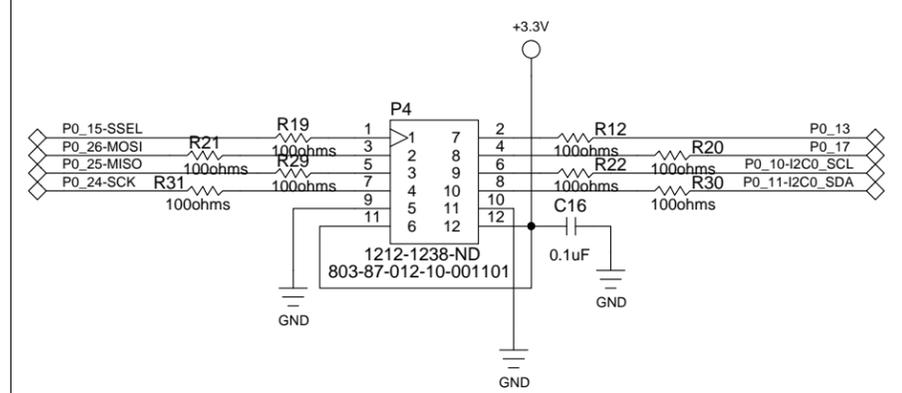


WAKEUP

| | | | |
|--------------|-------------|---|-------------------|
| CONTRACT NO. | | LPC82x Target MCU | |
| APPROVALS | DATE | NXP Semiconductors | |
| DRAWN | d.consiglio | 411 E. Plumeria Dr | |
| CHECKED | 7/8/2014 | San Jose, CA 95134 | |
| ISSUED | 5/01/14 | www.standardics.nxp.com/microcontrollers/ | |
| SCALE | D | SIZE/FSCM NO. | DWG. NO. |
| | | | LPC82x Xpresso v2 |
| | | | REV B |
| | | SHEET | 2 OF 03 |

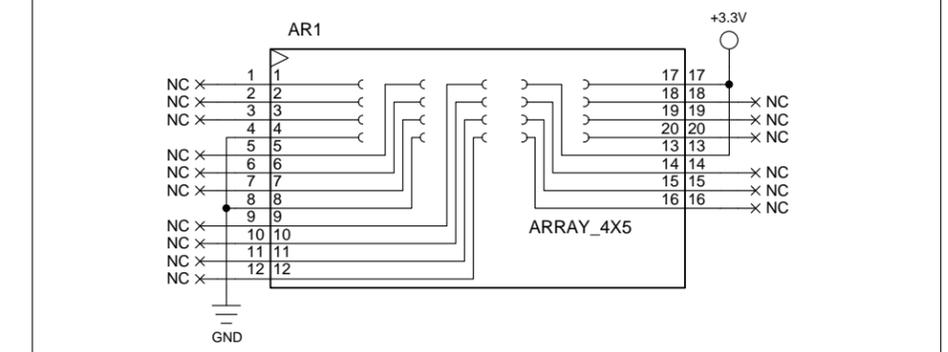
| REVISIONS | | | |
|-----------|-------------|------|----------|
| REV | DESCRIPTION | DATE | APPROVED |
| | | | |

PMOD compatible header

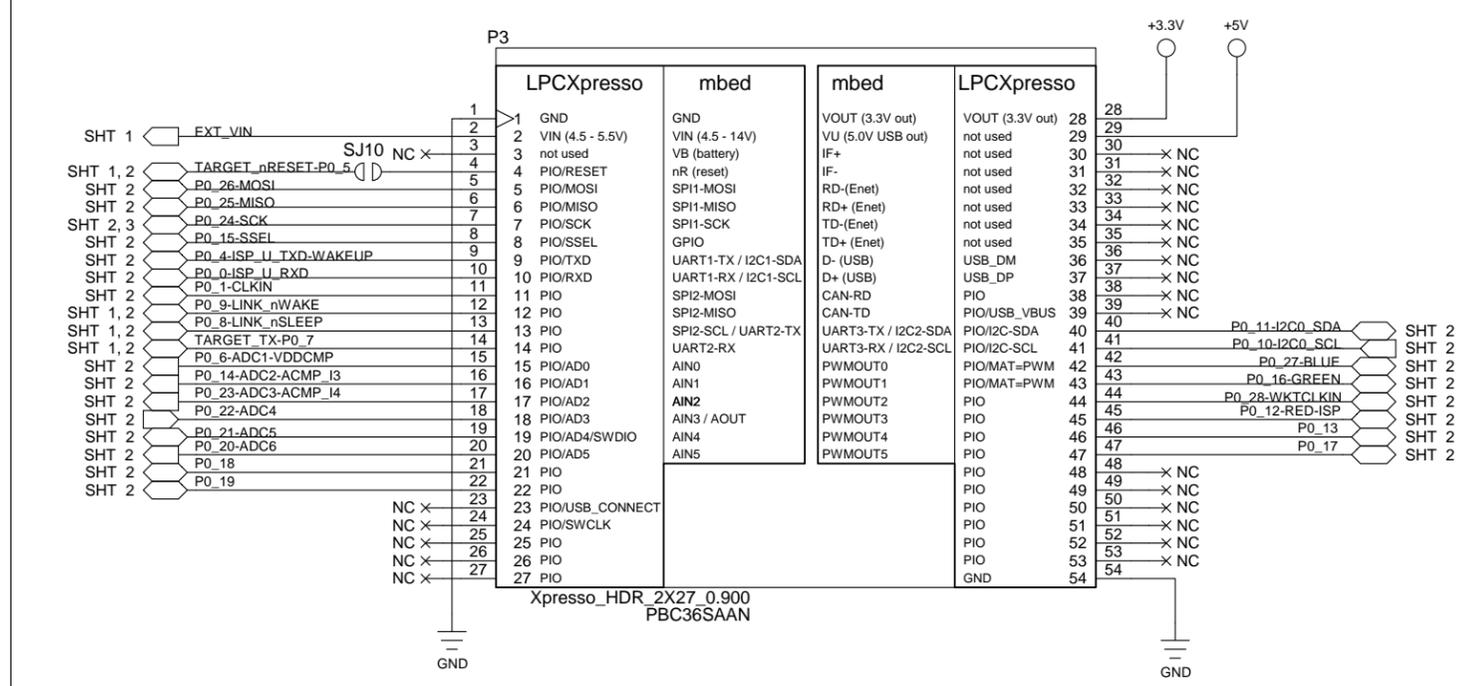


- PMOD pin function**
- Pin 1: GPIO/SPI-SSEL(out)/UART-CTS(in)
 - Pin 2: GPIO/SPI-MOSI(out)/UART-TXD(out)
 - Pin 3: GPIO/SPI-MISO(in)/UART-RXD(in)
 - Pin 4: GPIO/SPI-SCK(out)/UART-RTS(out)
 - Pin 5: GND
 - Pin 6: VCC(3.3V)
 - Pin 7: GPIO/INT(in)
 - Pin 8: GPIO/RESET(out)
 - Pin 9: GPIO/I2C_SCL
 - Pin 10: GPIO/I2C_SDA
 - Pin 11: GND
 - Pin 12: VCC(3.3V)

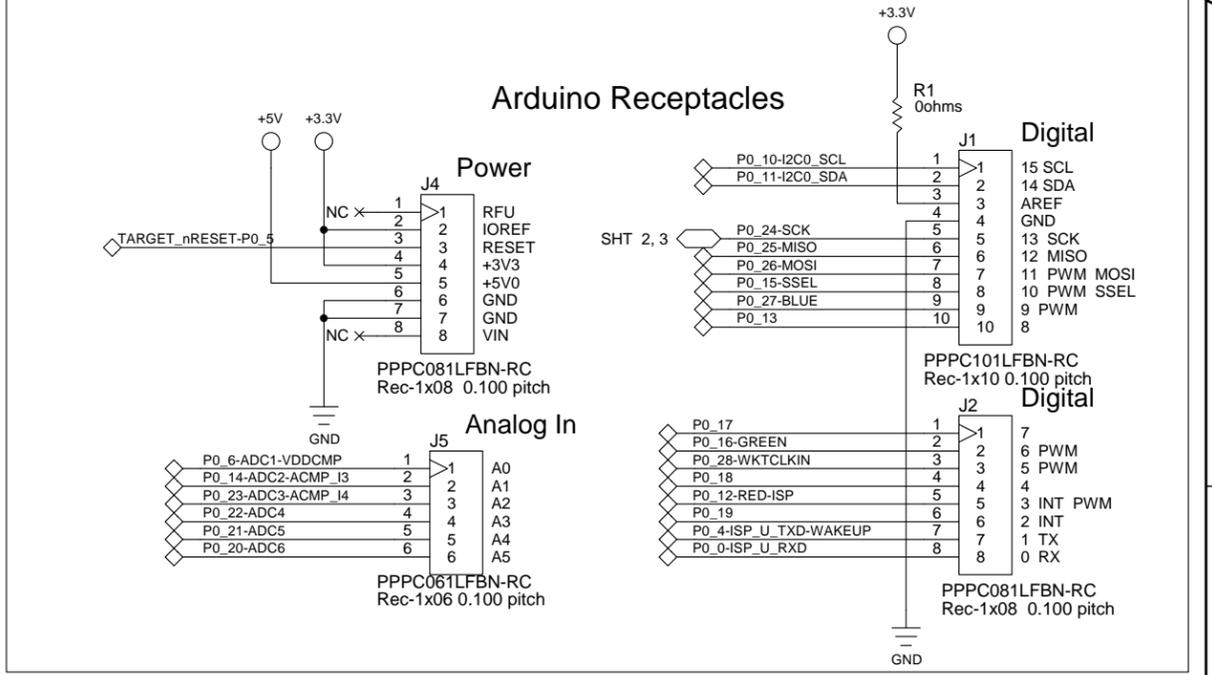
Breadboard hole grid



LPCXpresso header (Bottom side of PCB)



Arduino Receptacles



| | | | |
|--------------|-------------|--|-------------------|
| CONTRACT NO. | | Arduino/ Xpresso/EA Serial/Breadboard headers | |
| APPROVALS | DATE | NXP Semiconductors 411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/ | |
| DRAWN | d.consiglio | | |
| CHECKED | | SIZE | D |
| ISSUED | 5/01/14 | FWSCM NO. | LPC82x Xpresso v2 |
| | | DWG. NO. | B |
| | | SCALE | SHEET 3 OF 03 |