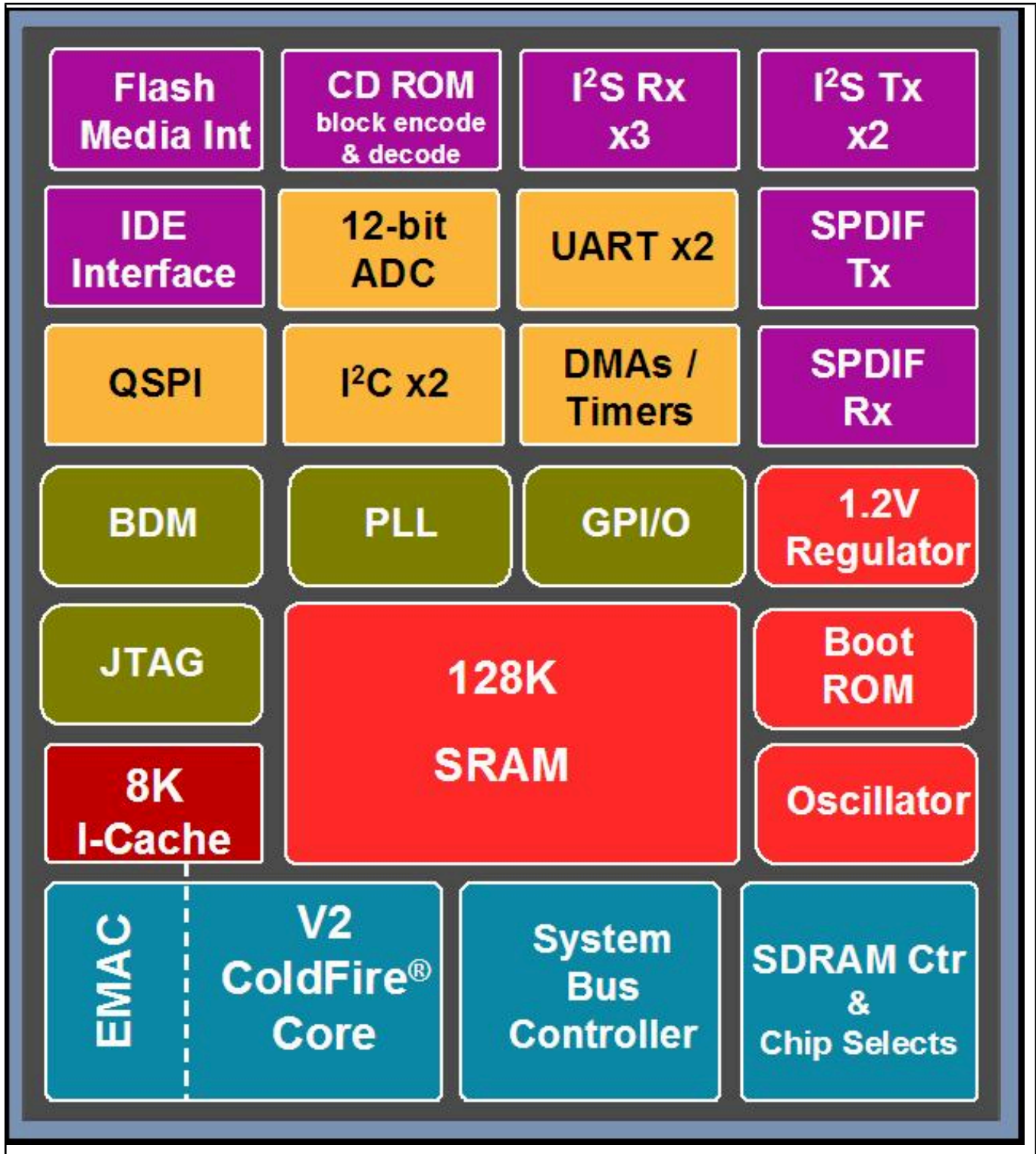# Integrated ColdFire® V2 Microprocessor

SCF5250

建议不要在新设计中使用该软件
建议不要在新设计中使用本页介绍的产品。

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The SCF5250 was designed as a system controller/decoder for compressed audio music players (MP3, WMA, OggVorbis, etc.), addressing both portable and automotive solutions supporting CD-and HDD-based systems. The 32-bit ColdFire® V2 core with enhanced multiplier-accumulator (eMAC) unit provides optimum performance and code density for the combination of control code and signal processing required for compressed audio decode, file management and system control. The SCF5250 is also an excellent general purpose system controller with over 107 (Dhrystone 2.1) MIPS @ 120 MHz performance and a competitive price. The integrated peripherals and eMAC allow the SCF5250 to replace both the microcontroller and the DSP in certain applications. Many peripheral pins can also be remapped as general purpose I/O (GPIO) pins.

SCF5250 Block Diagram Block Diagram

Flash Media Int	CD ROM block encode & decode	I²S Rx x3	I²S Tx x2
IDE Interface	12-bit ADC	UART x2	SPDIF Tx
QSPI	I²C x2	DMAs / Timers	SPDIF Rx
BDM	PLL	GPI/O	1.2V Regulator
JTAG	128K SRAM		Boot ROM
8K I-Cache			Oscillator
EMAC V2 ColdFire® Core		System Bus Controller	SDRAM Ctr & Chip Selects

View additional information for Integrated ColdFire® V2 Microprocessor.

Note: The information on this document is subject to change without notice.

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