



MPC5777C Battery Management Systems (BMS) and Engine Control Development Board 2

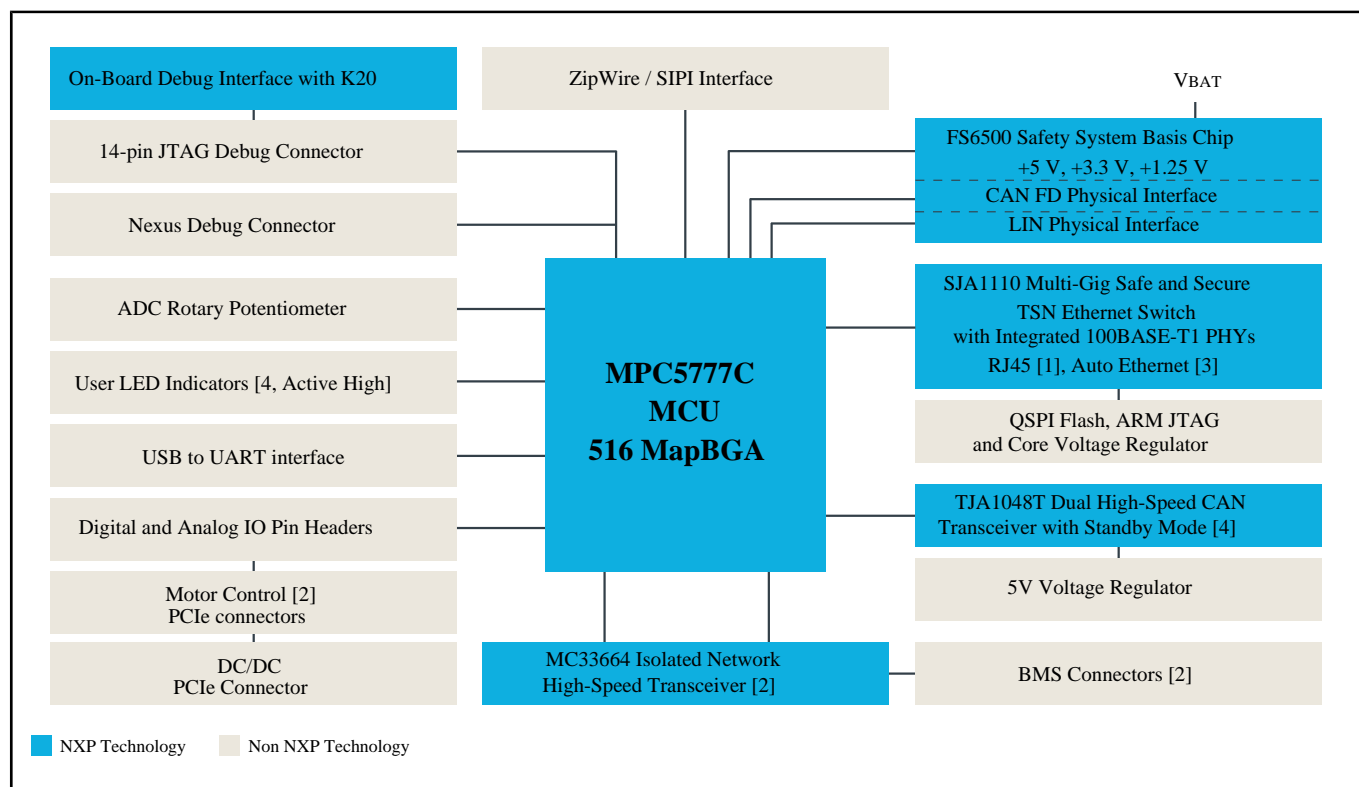
MPC5777C-DEVB2

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The MPC5777C-DEVB2 is a development board engineered for a range of applications: from traction motors and battery management systems (BMS) to internal combustion engines and transmissions.

Based on the 32-bit Power Architecture® MPC5777C ultrareliable microcontroller, together with the FS65 system basis chip (SBC), the SJA1110 and TJA1048T Ethernet and CAN transceivers, the MPC5777C-DEVB2 facilitates hardware and software development, offering system performance, safety and security.

MPC5777C-DEVB2 Block Diagram Block Diagram



View additional information for [MPC5777C Battery Management Systems \(BMS\) and Engine Control Development Board 2](#).

Note: The information on this document is subject to change without notice.

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